

PDF SCHEMATICS TABLE OF CONTENTS (Current Revisions)

1. AC Distribution

AC Dist-D	3
-----------------	---

2. Amp Board

Amp-e5 Oil Phase	4
Amp-f1	5
Amp-f2	6
Amp-f3	7
Amp-f4	8
Amp-f5	9
Amp-f6	10
Amp-f7	11

3. EPC

EPCHI-b1 EPC	12
EPCHI-b2 FPD	13
EPCHI-b2 HID	14
EPCHI-b2 PID	15
EPCHI-b2.....	16
EPC-j	17

4. Display Board

110 Display-b1	18
110 Display-b2	19
310 Display-d1	20
310 Display-d2	21
310 Display-d3.....	22
8610 Display-k1.....	23
8610 Display-k2.....	24
8610 Display-k3.....	25
8610 Display-k4.....	26
8610 Display-k5.....	27

5. Heat

a. Heat Board

Heat-j1.....	28
Heat-j2.....	29

b. Oven

Oven Cool Down_Gate Circuit	30
Oven-i Vertical Mod	31
Oven-i	32
Dual Oven-g1	33
Dual Oven-g2	34
Dual Oven-g3	35

c. ION Amp

Ion Amp-a2	36
------------------	----

d. Purge & Trap

P&T-d1	37
P&T-d2	38

6. Layout

310 Layout	39
8610-C Layout-k.....	40

7. Relay

203 Relay-b	41
Relay-c1	42
Relay-c2	43

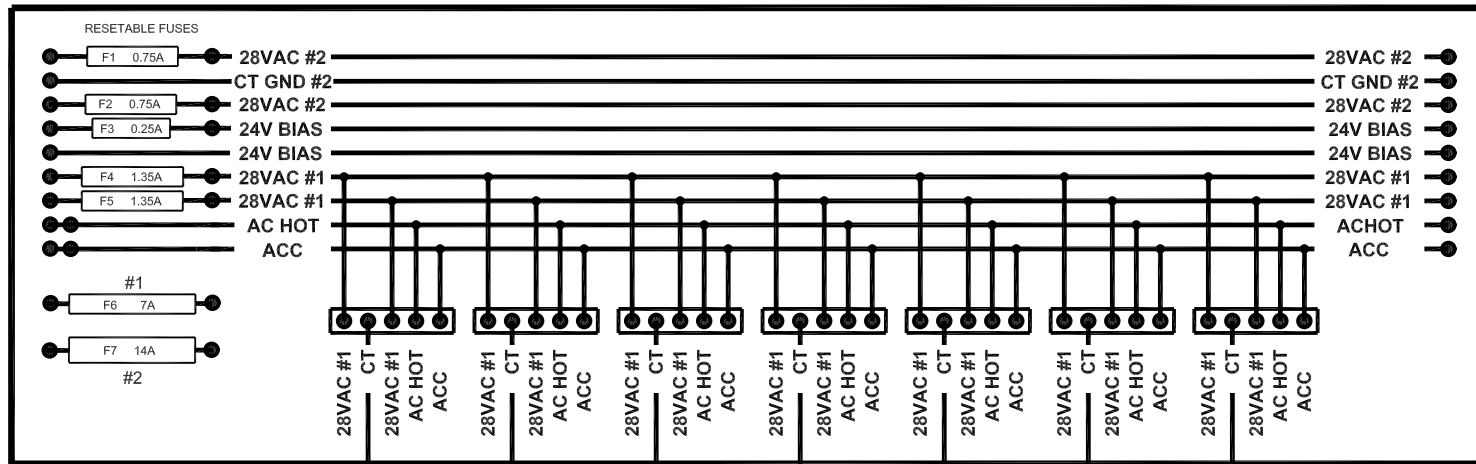
8. Serps Board

Serps-g1	44
Serps-g2	45

8610 AC DISTRIBUTION BOARD

**INPUT
FROM
X-FORMER
AND
INTERLOCK
SWITCH**

NOTE: F7 (14A FUSE)
IS USED FOR THE DELCDBRIDGE



**OUTPUT
TO
AMP
BOARD**

MAIN CHASSIS
GROUND STUD

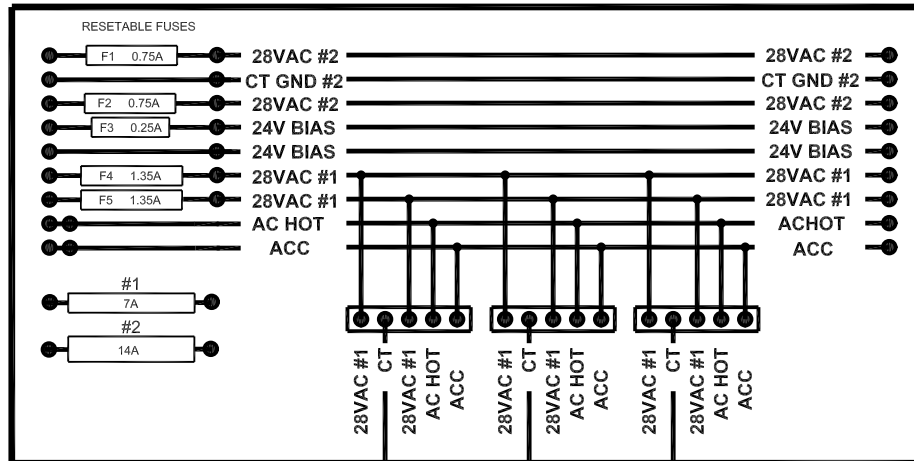
SEPARATE GROUND WIRES

USED IN THE MODEL 8610
GAS CHROMATOGRAPH

310 AC DISTRIBUTION BOARD

**INPUT
FROM
X-FORMER
AND
INTERLOCK
SWITCH**

NOTE: #2 (14A FUSE)
IS USED FOR THE DELCDBRIDGE



**OUTPUT
TO
AMP
BOARD**

MAIN CHASSIS
GROUND STUD

SEPARATE GROUND WIRES

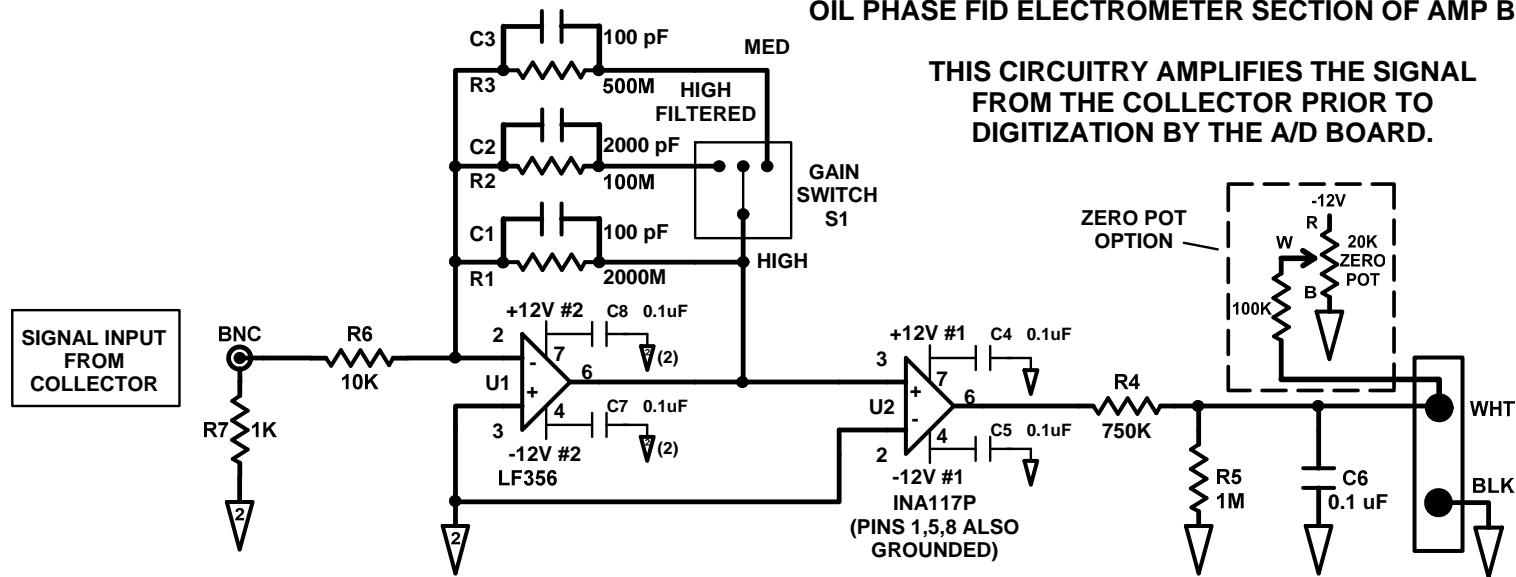
USED IN THE MODEL 310
GAS CHROMATOGRAPH

Page 1 of 1	Bd. REV.	AC DISTRIBUTION SCHEMATIC	
	8610 - D 310 - B		
Filename: ACDIST-ca.tcw	Date: 12/20/97	By: R. Fenske	
	REV. Date: 5/13/02	By: M. Watts	

OIL PHASE FID ELECTROMETER SECTION OF AMP BOARD

THIS CIRCUITRY AMPLIFIES THE SIGNAL FROM THE COLLECTOR PRIOR TO DIGITIZATION BY THE A/D BOARD.

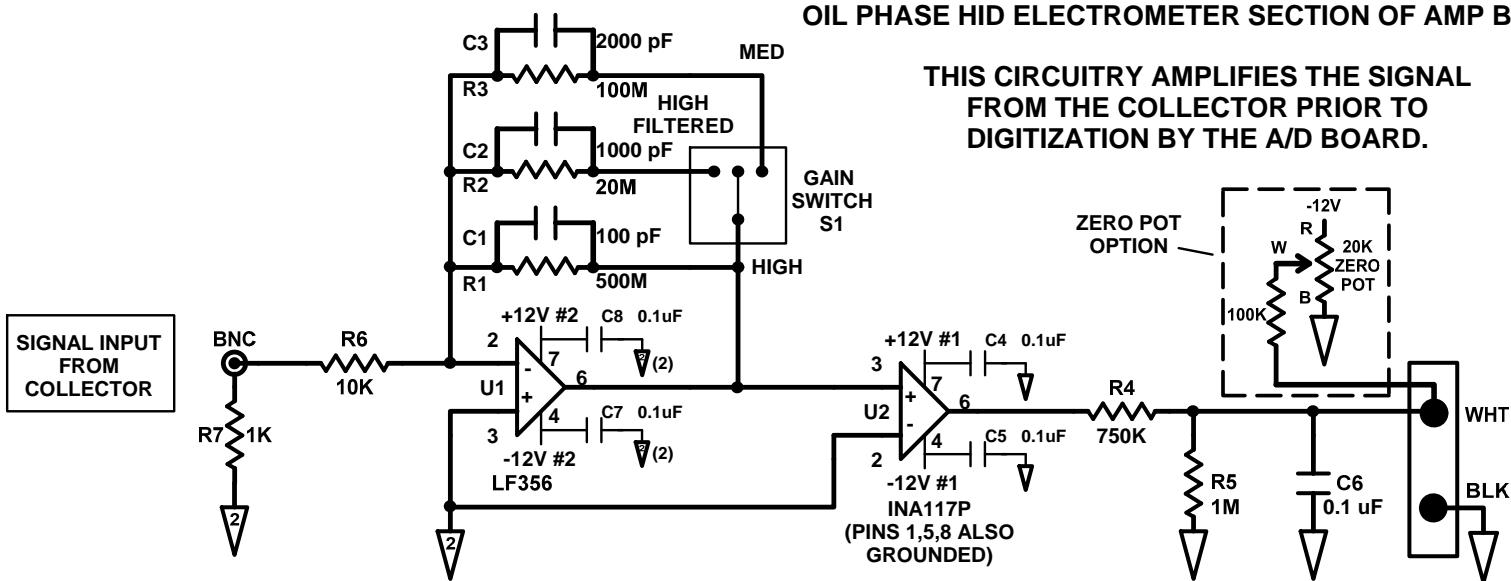
NOTE: MODEL 310 STUDENT TCD G.C.s AND MODEL 110 STANDALONE DETECTORS DO NOT HAVE AN A/D BOARD. THESE MODELS REQUIRE ZERO POTS AND ATTENUATORS TO ADJUST THE DETECTOR OUTPUTS TO THEIR DESIRED LEVEL. SEE THE "ZERO POTS AND ATTENUATORS" PAGE IN THIS SECTION.



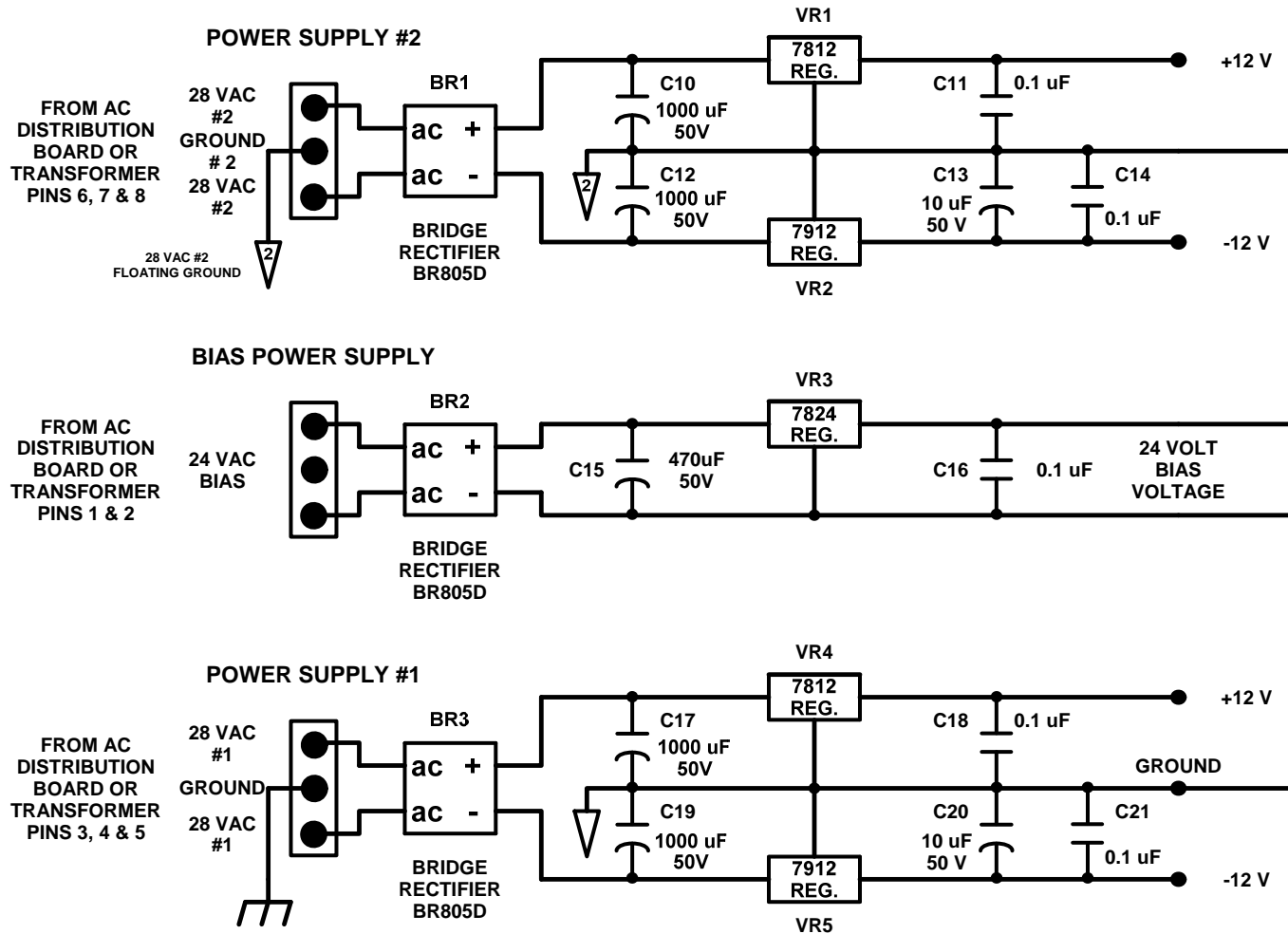
OIL PHASE HID ELECTROMETER SECTION OF AMP BOARD

THIS CIRCUITRY AMPLIFIES THE SIGNAL FROM THE COLLECTOR PRIOR TO DIGITIZATION BY THE A/D BOARD.

NOTE: MODEL 310 STUDENT TCD G.C.s AND MODEL 110 STANDALONE DETECTORS DO NOT HAVE AN A/D BOARD. THESE MODELS REQUIRE ZERO POTS AND ATTENUATORS TO ADJUST THE DETECTOR OUTPUTS TO THEIR DESIRED LEVEL. SEE THE "ZERO POTS AND ATTENUATORS" PAGE IN THIS SECTION.



POWER SUPPLY SECTION OF AMP BOARD

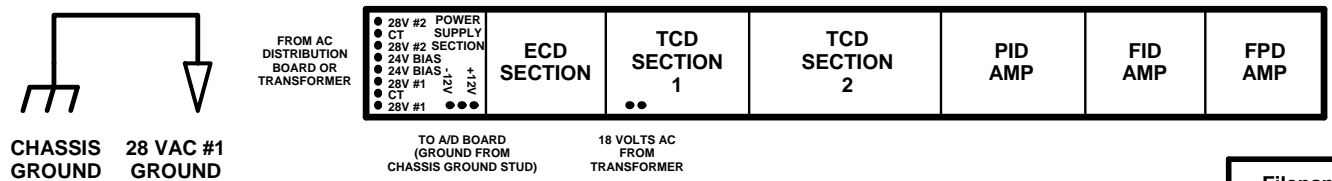


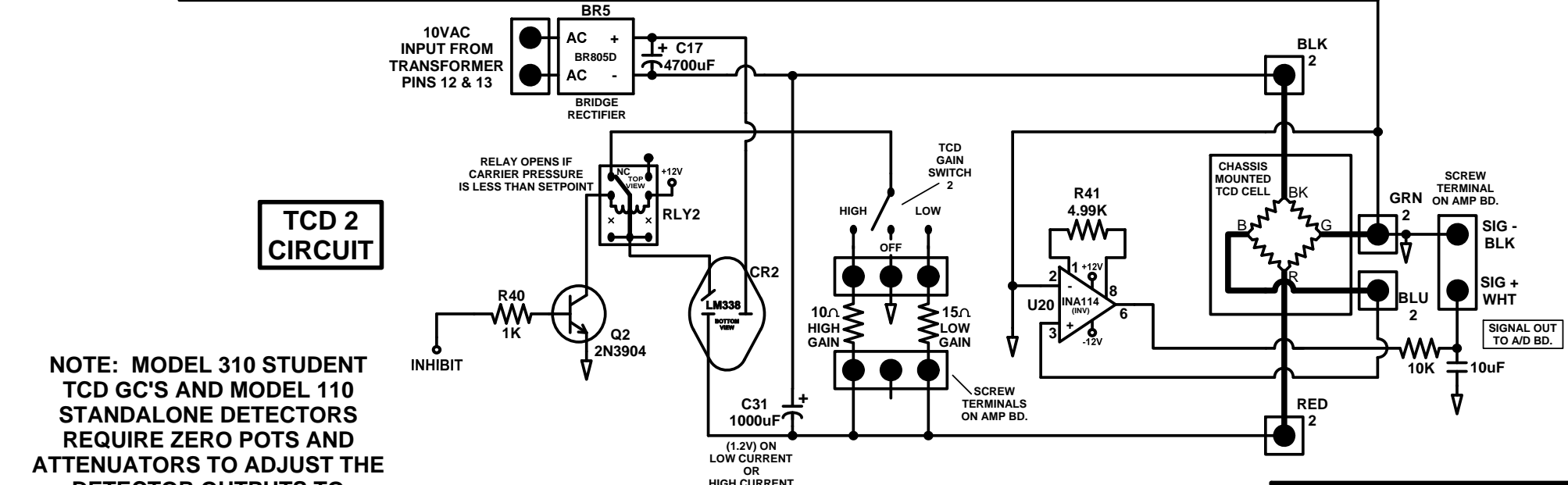
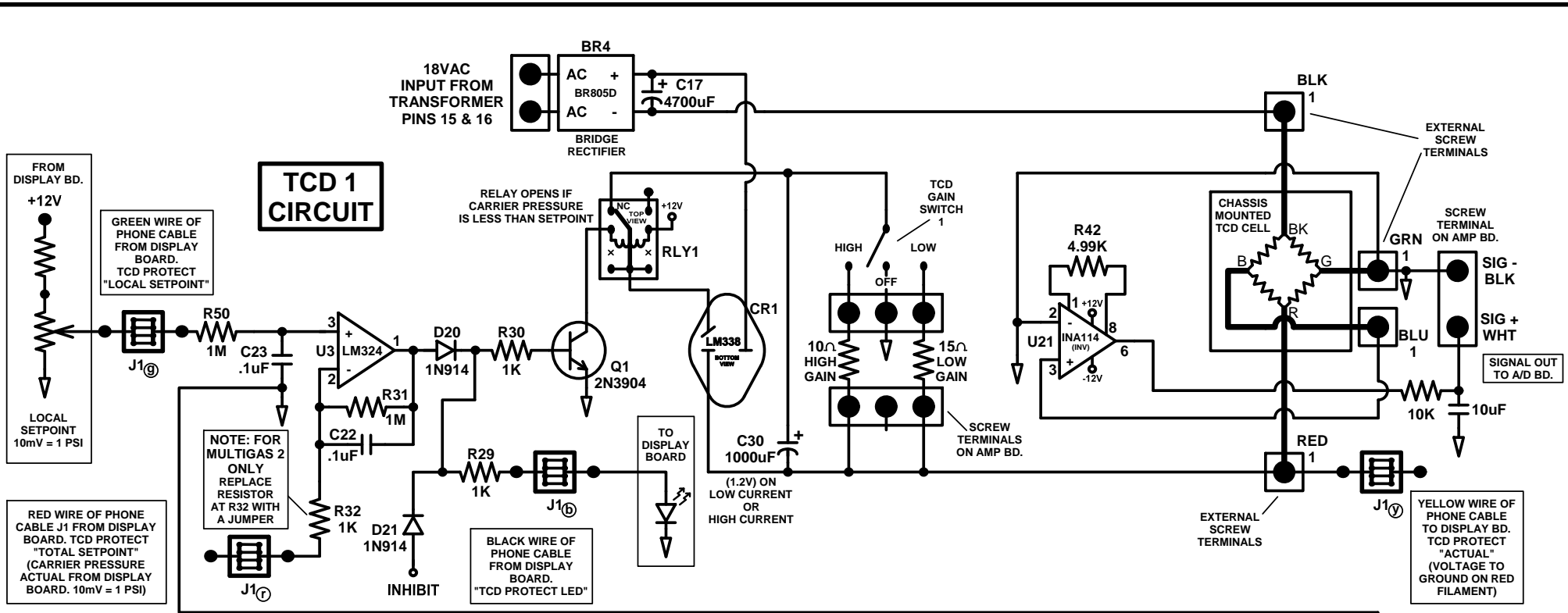
POWER SUPPLY #2 IS A +12 V AND -12 V SUPPLY BIASED 24 VOLTS ABOVE GROUND POTENTIAL BY VR3 OF THE BIAS POWER SUPPLY. THIS SUPPLY IS USED BY THE FID, PID, AND FPD DETECTOR AMPLIFIERS.

THE BIAS POWER SUPPLY MAINTAINS A 24 VOLT DC POTENTIAL BETWEEN GROUND #1 AND GROUND #2.

POWER SUPPLY #1 IS THE PRIMARY POWER SOURCE FOR ALL FUNCTIONS OF THE AMP BOARD AND ALSO PROVIDES +12 V AND -12 V TO THE A/D BOARD. ONLY THE TCD CIRCUIT REQUIRES AN ADDITIONAL AC VOLTAGE INPUT. SEE PAGE 2.

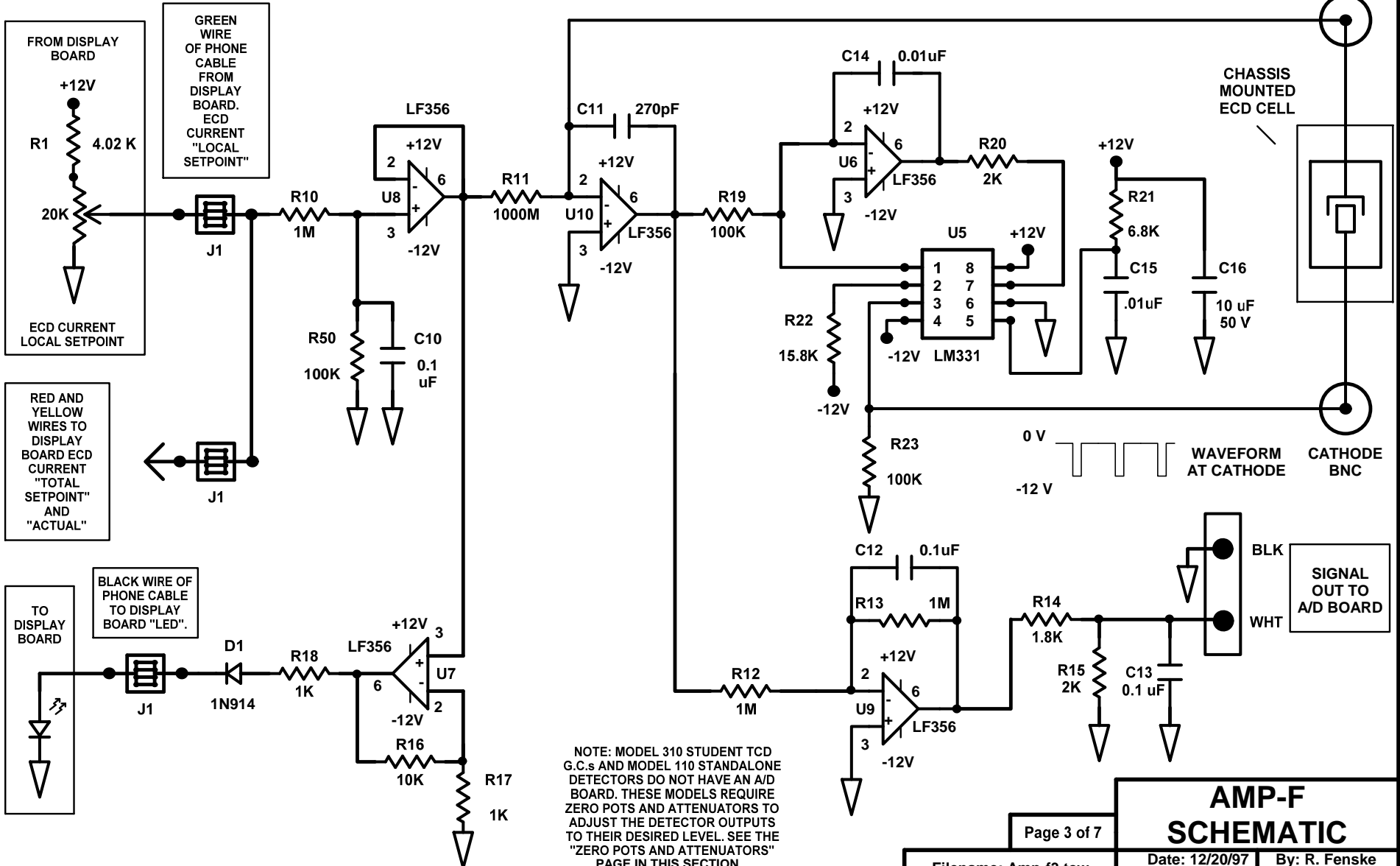
AMPLIFIER BOARD LAYOUT





NOTE: MODEL 310 STUDENT TCD GC'S AND MODEL 110 STANDALONE DETECTORS REQUIRE ZERO POTS AND ATTENUATORS TO ADJUST THE DETECTOR OUTPUTS TO THEIR DESIRED LEVEL. SEE THE "ZERO POTS AND ATTENUATORS" PAGE IN THIS SECTION.

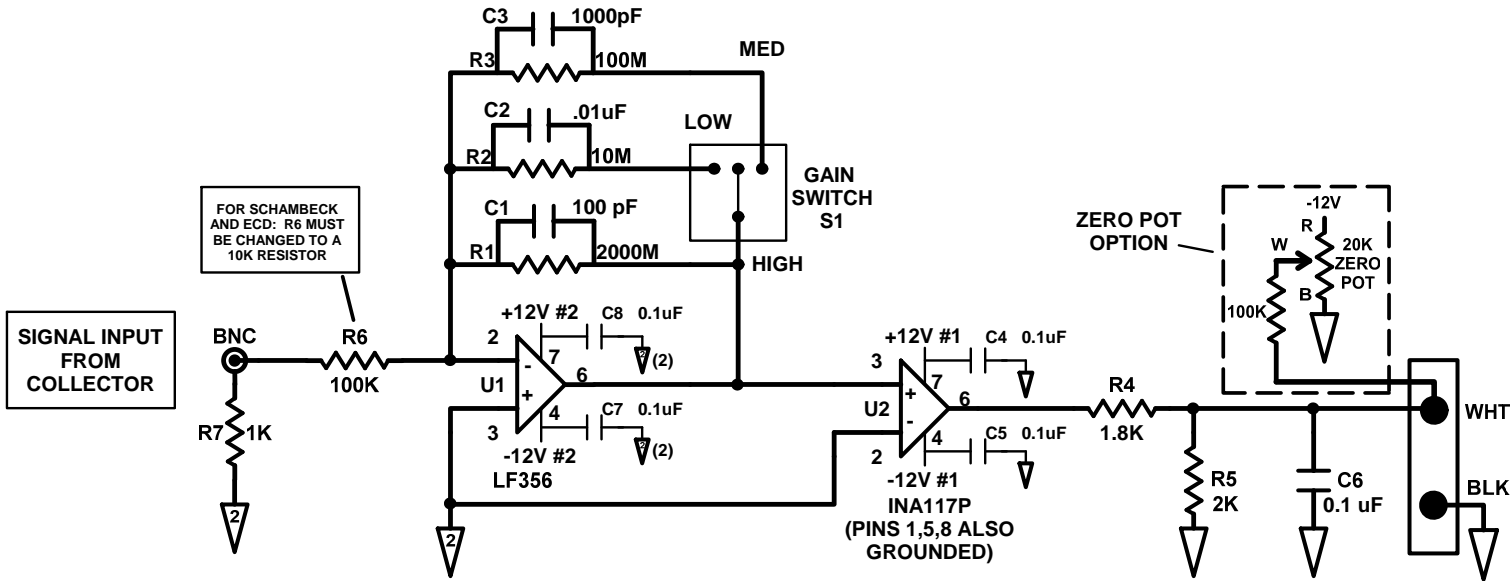
ECD CURRENT SECTION OF AMP BOARD



NOTE: MODEL 310 STUDENT TCD G.C.s AND MODEL 110 STANDALONE DETECTORS DO NOT HAVE AN A/D BOARD. THESE MODELS REQUIRE ZERO POTS AND ATTENUATORS TO ADJUST THE DETECTOR OUTPUTS TO THEIR DESIRED LEVEL. SEE THE "ZERO POTS AND ATTENUATORS" PAGE IN THIS SECTION.

FID, NPD, HID, TID
ELECTROMETER SECTION OF AMP BOARD

THIS CIRCUITRY AMPLIFIES THE SIGNAL
FROM THE COLLECTOR PRIOR TO
DIGITIZATION BY THE A/D BOARD.



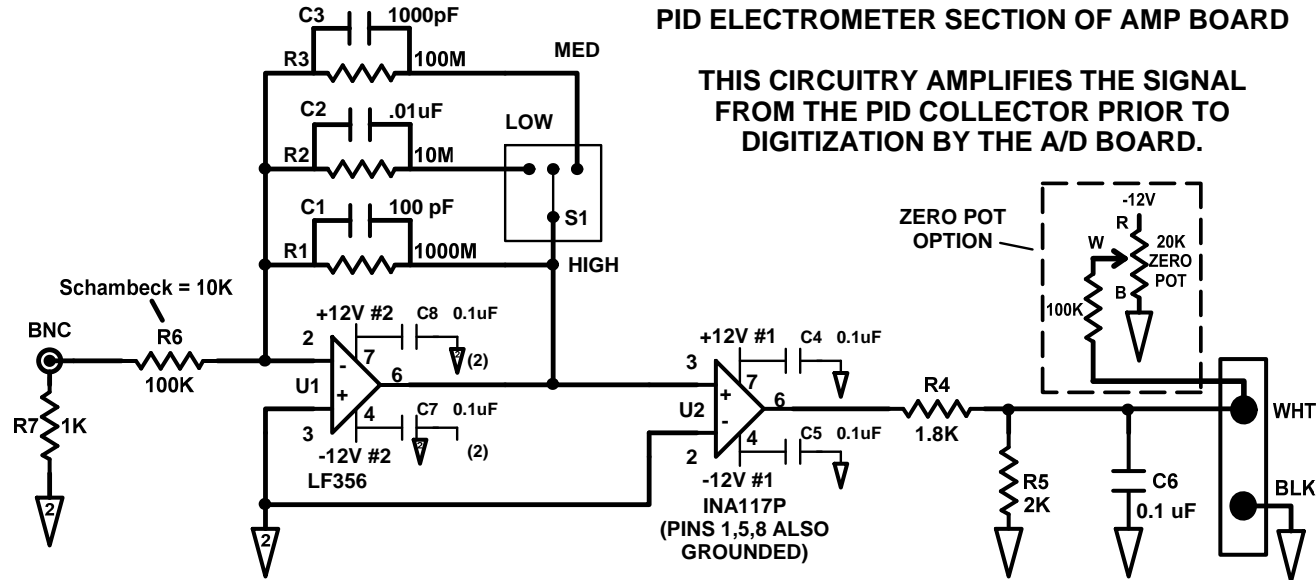
NOTE: MODEL 310 STUDENT TCD G.C.s AND MODEL 110 STANDALONE DETECTORS DO NOT HAVE AN A/D BOARD. THESE MODELS REQUIRE ZERO POTS AND ATTENUATORS TO ADJUST THE DETECTOR OUTPUTS TO THEIR DESIRED LEVEL. SEE THE "ZERO POTS AND ATTENUATORS" PAGE IN THIS SECTION.

PID ELECTROMETER SECTION OF AMP BOARD

THIS CIRCUITRY AMPLIFIES THE SIGNAL FROM THE PID COLLECTOR PRIOR TO DIGITIZATION BY THE A/D BOARD.

NOTE: MODEL 310 STUDENT TCD G.C.s AND MODEL 110 STANDALONE DETECTORS DO NOT HAVE AN A/D BOARD. THESE MODELS REQUIRE ZERO POTS AND ATTENUATORS TO ADJUST THE DETECTOR OUTPUTS TO THEIR DESIRED LEVEL. SEE THE "ZERO POTS AND ATTENUATORS" PAGE IN THIS SECTION.

SIGNAL INPUT FROM PID COLLECTOR



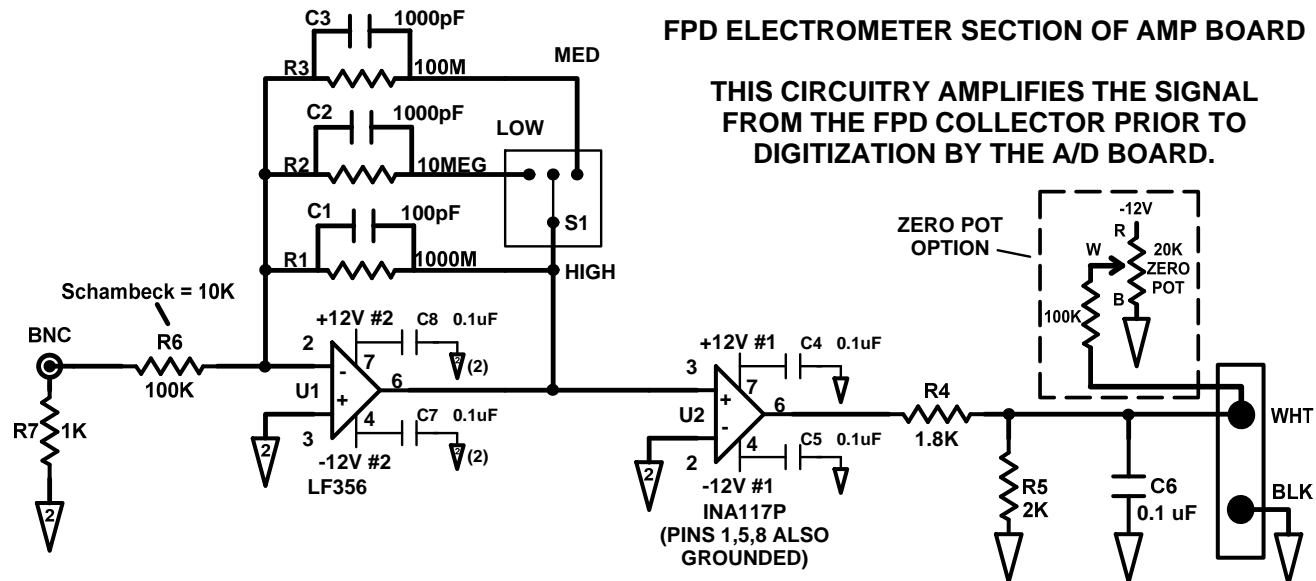
SIGNAL OUT TO A/D BOARD

FPD ELECTROMETER SECTION OF AMP BOARD

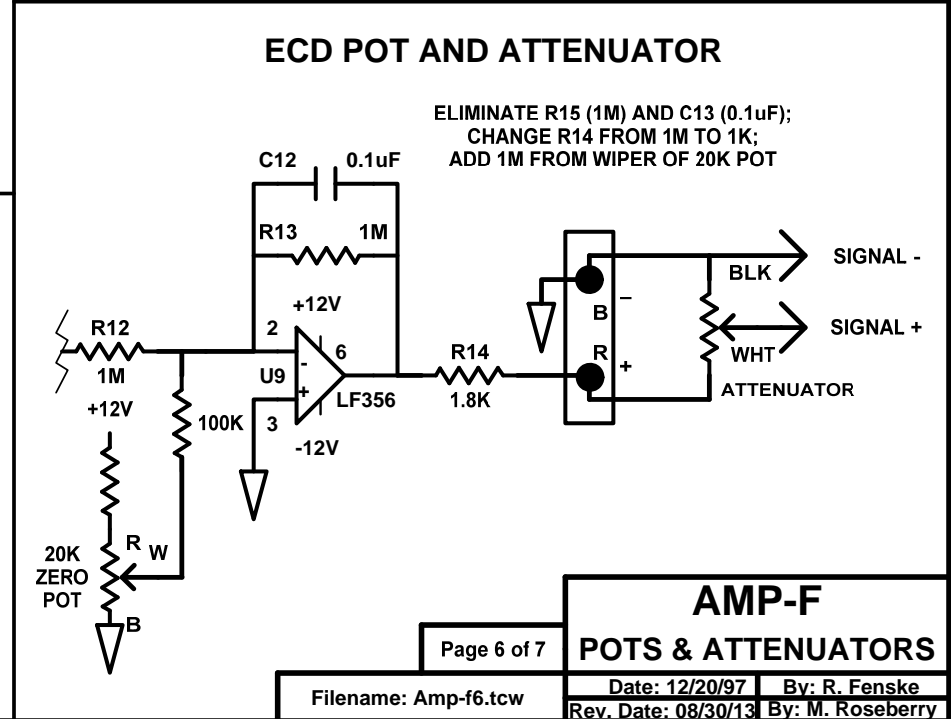
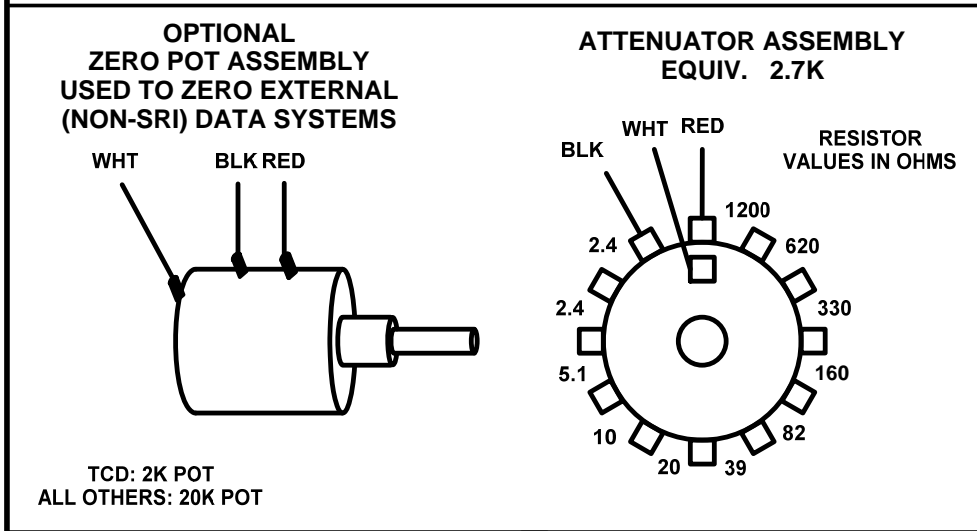
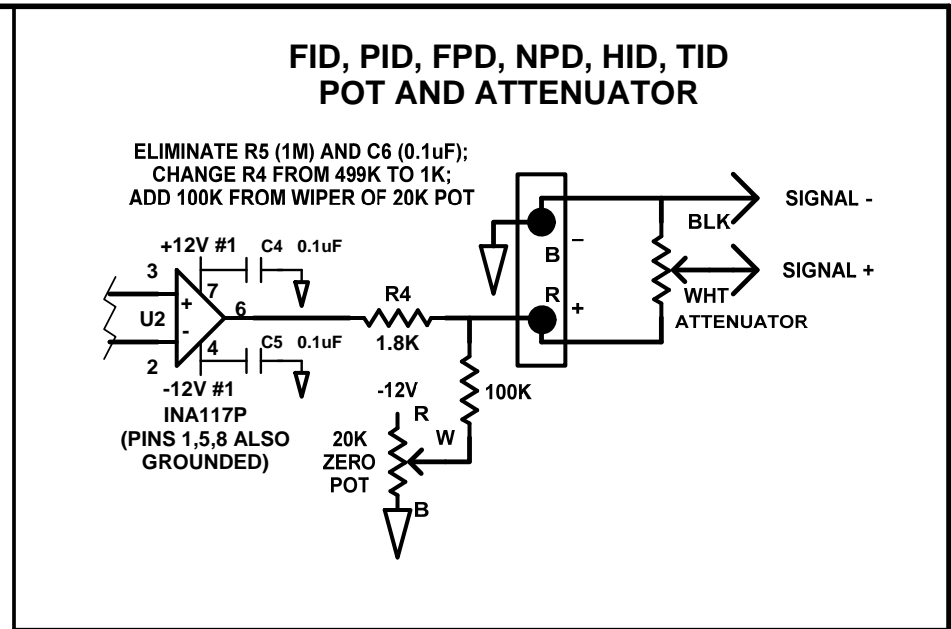
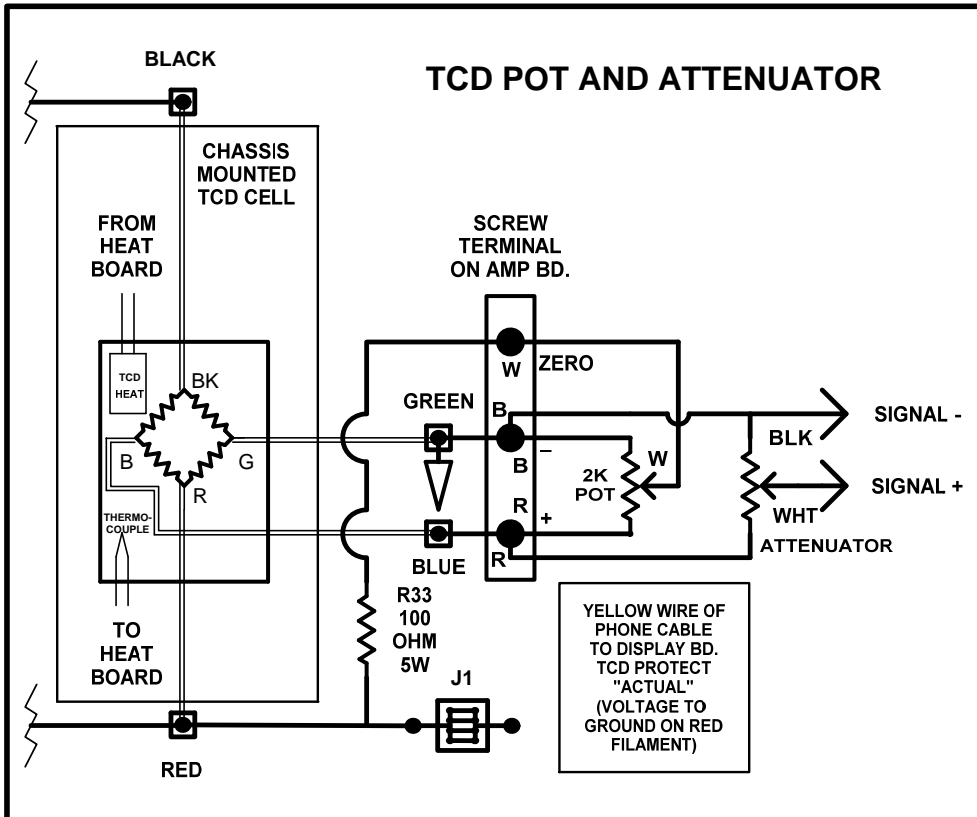
THIS CIRCUITRY AMPLIFIES THE SIGNAL FROM THE FPD COLLECTOR PRIOR TO DIGITIZATION BY THE A/D BOARD.

NOTE: MODEL 310 STUDENT TCD G.C.s AND MODEL 110 STANDALONE DETECTORS DO NOT HAVE AN A/D BOARD. THESE MODELS REQUIRE ZERO POTS AND ATTENUATORS TO ADJUST THE DETECTOR OUTPUTS TO THEIR DESIRED LEVEL. SEE THE "ZERO POTS AND ATTENUATORS" PAGE IN THIS SECTION.

SIGNAL INPUT FROM FPD COLLECTOR

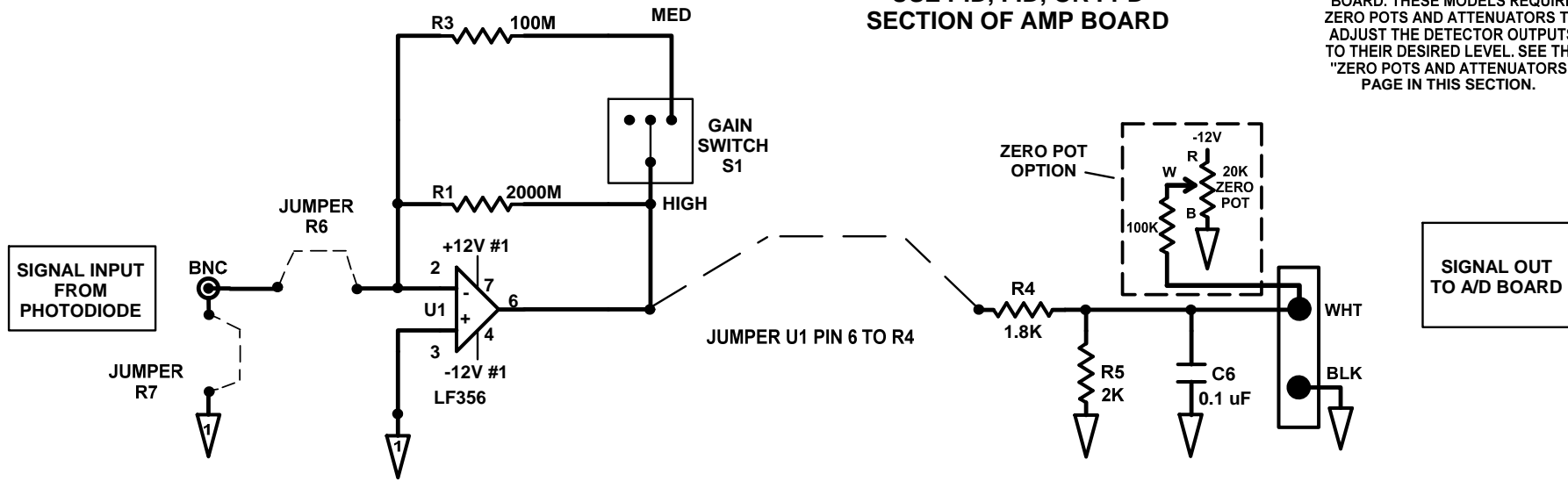


SIGNAL OUT TO A/D BOARD



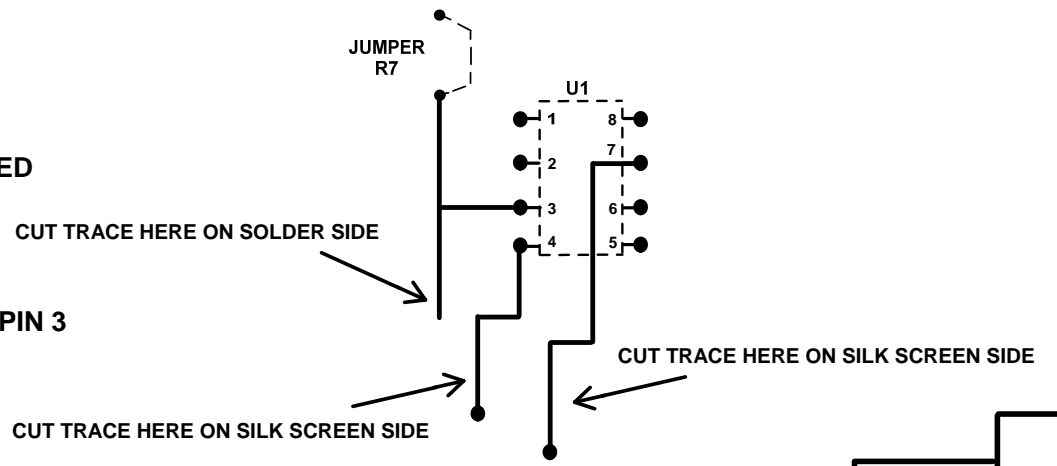
**RGD OPTION
USE PID, FID, OR FPD
SECTION OF AMP BOARD**

NOTE: MODEL 310 STUDENT TCD G.C.s AND MODEL 110 STANDALONE DETECTORS DO NOT HAVE AN A/D BOARD. THESE MODELS REQUIRE ZERO POTS AND ATTENUATORS TO ADJUST THE DETECTOR OUTPUTS TO THEIR DESIRED LEVEL. SEE THE "ZERO POTS AND ATTENUATORS" PAGE IN THIS SECTION.

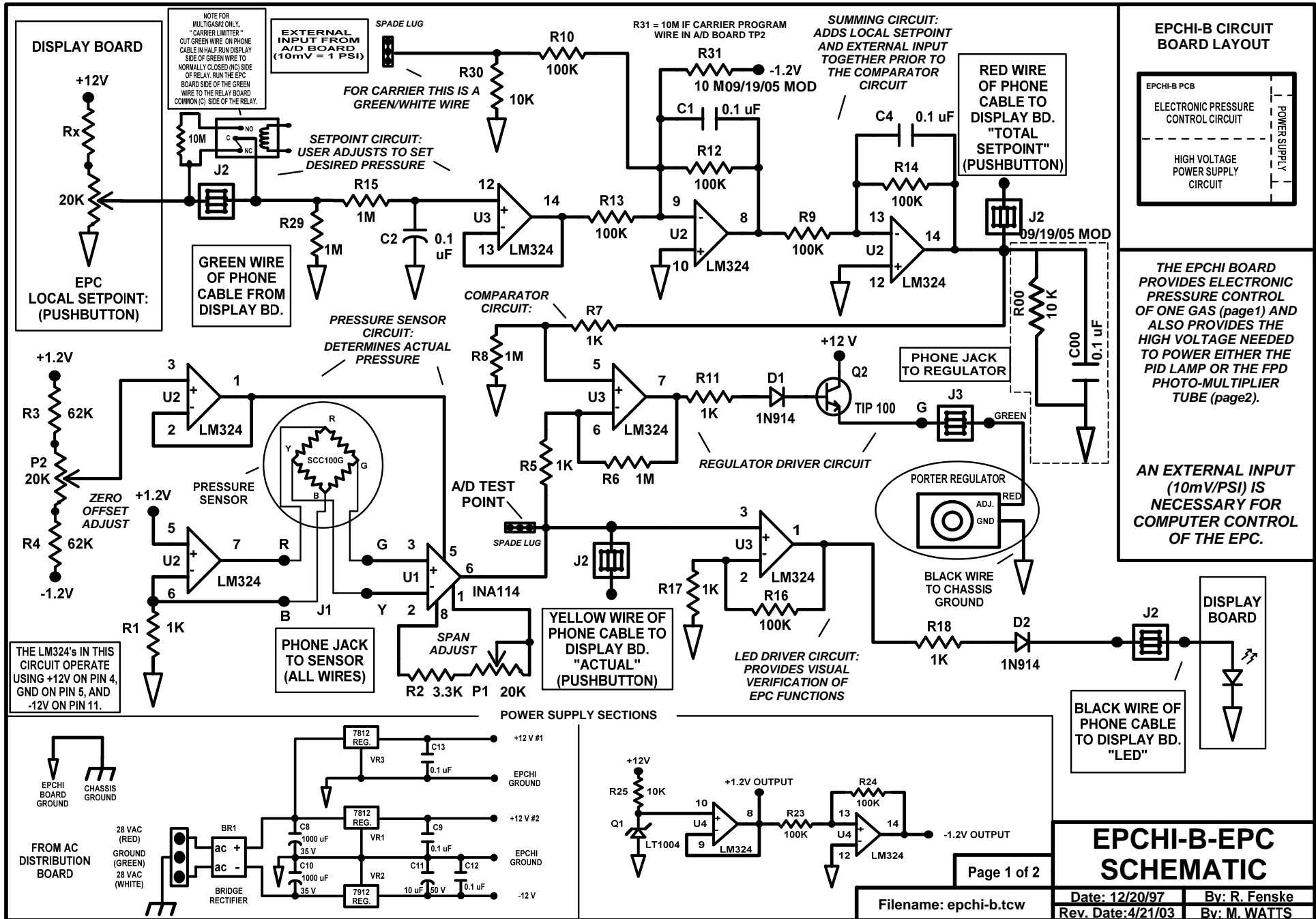


REMOVE THE 24V BIAS ON THE AMP BOARD BY CUTTING THREE TRACES TO THE SECTION TO BE USED
 CUT THE TRACE TO U1 PIN 3
 CUT THE TRACE TO U1 PIN 4
 CUT THE TRACE TO U1 PIN 7

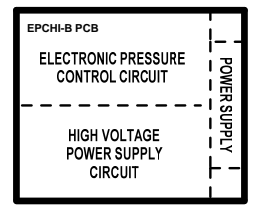
JUMP GND #1 TO TRACE GOING TO U1 PIN 3
 JUMP -12VDC #1 TO U1 PIN 4
 JUMP +12VDC #1 TO U1 PIN 7



**AMP-F
SCHEMATIC**



EPCH-B CIRCUIT BOARD LAYOUT

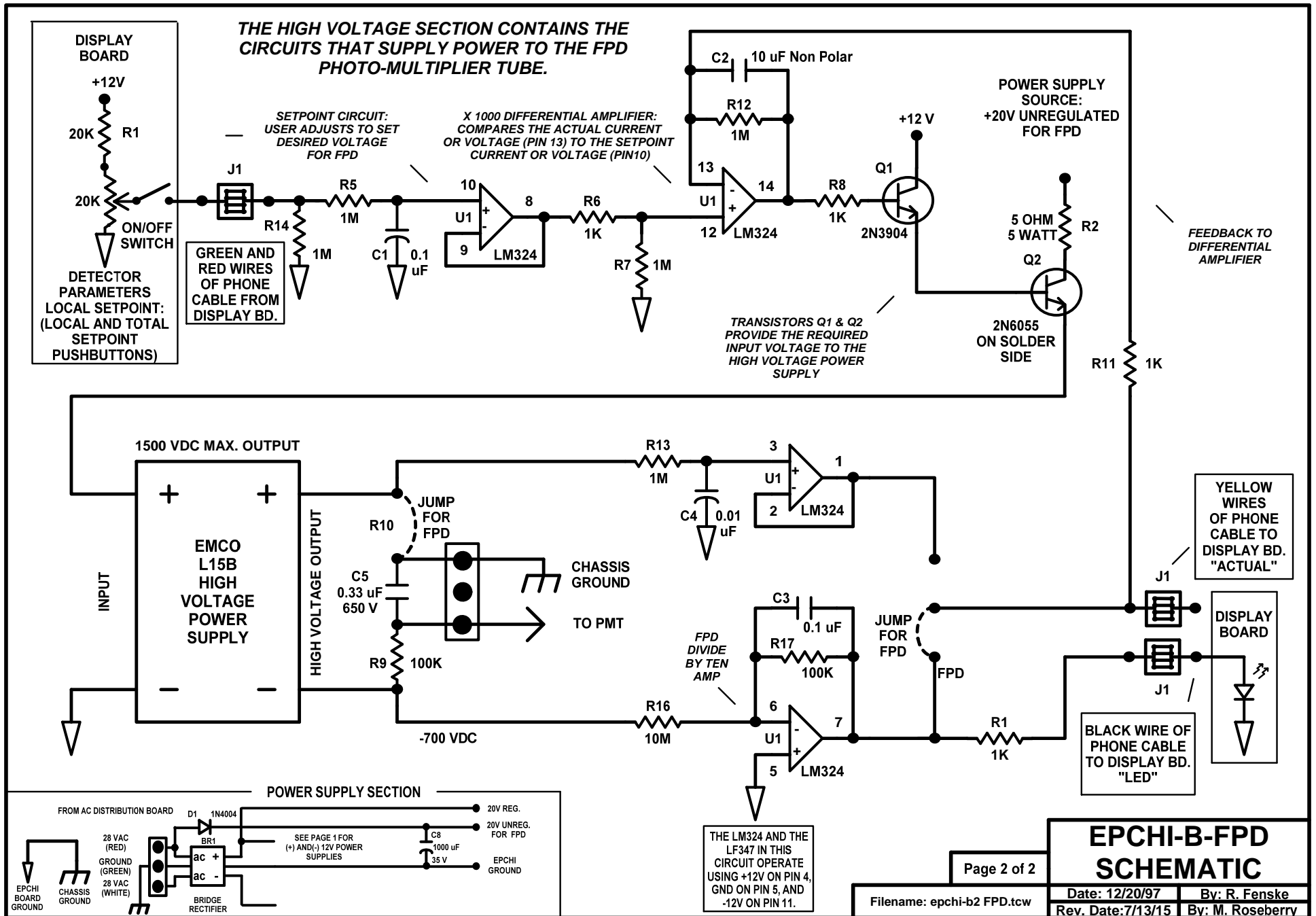


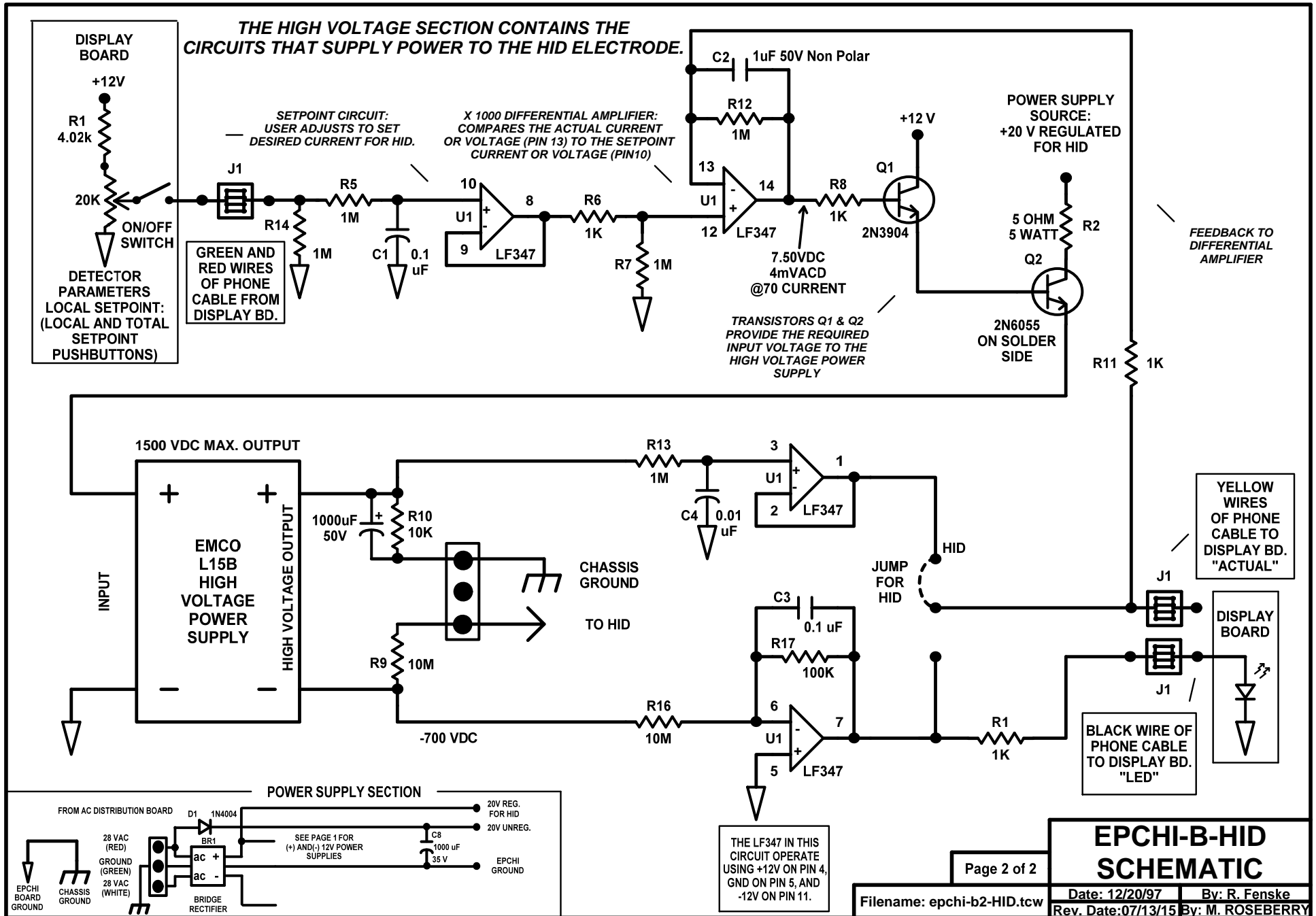
THE EPCH BOARD PROVIDES ELECTRONIC PRESSURE CONTROL OF ONE GAS (page1) AND ALSO PROVIDES THE HIGH VOLTAGE NEEDED TO POWER EITHER THE PID LAMP OR THE FPD PHOTO-MULTIPLIER TUBE (page2).

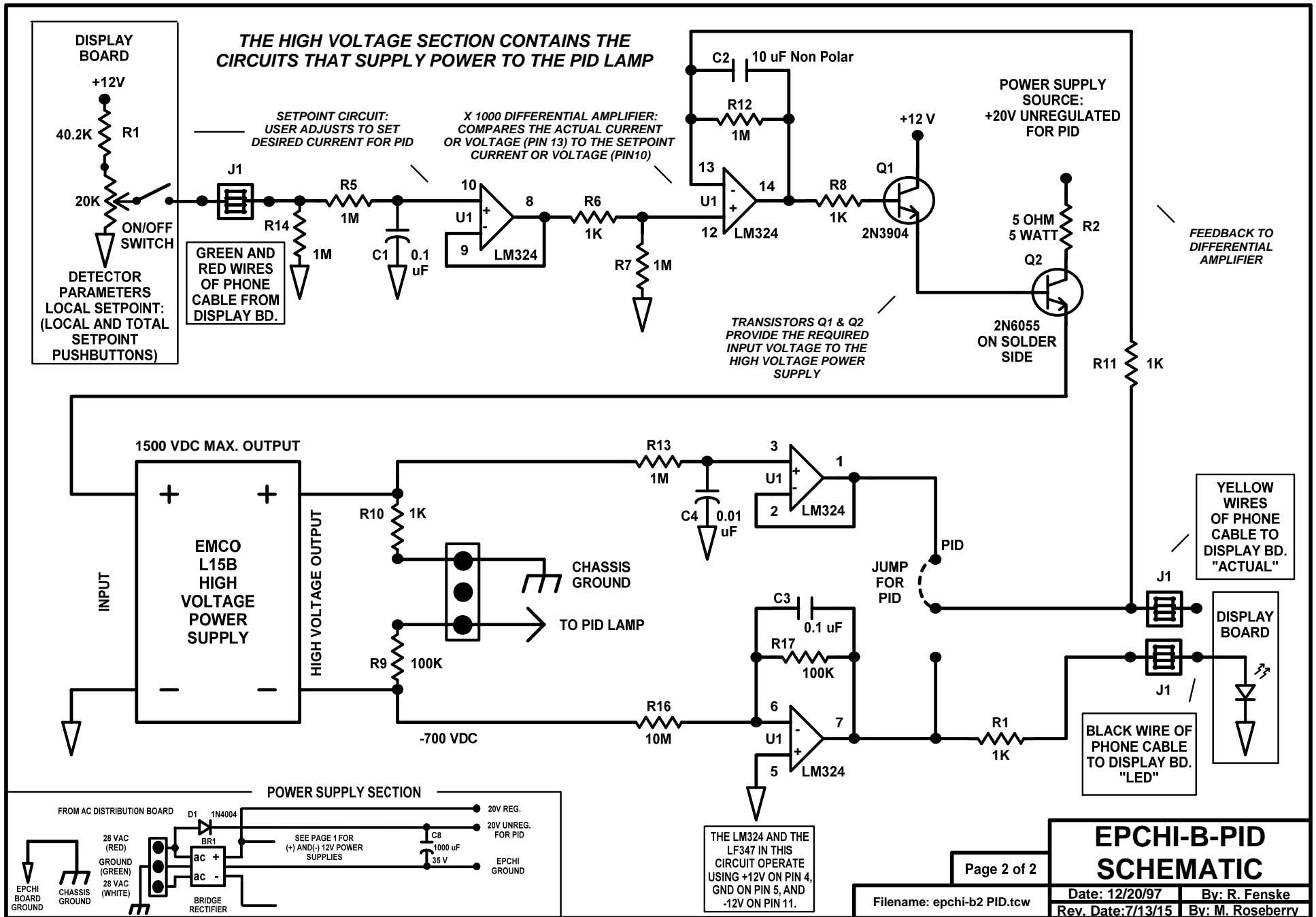
AN EXTERNAL INPUT (10mV/PSI) IS NECESSARY FOR COMPUTER CONTROL OF THE EPC.

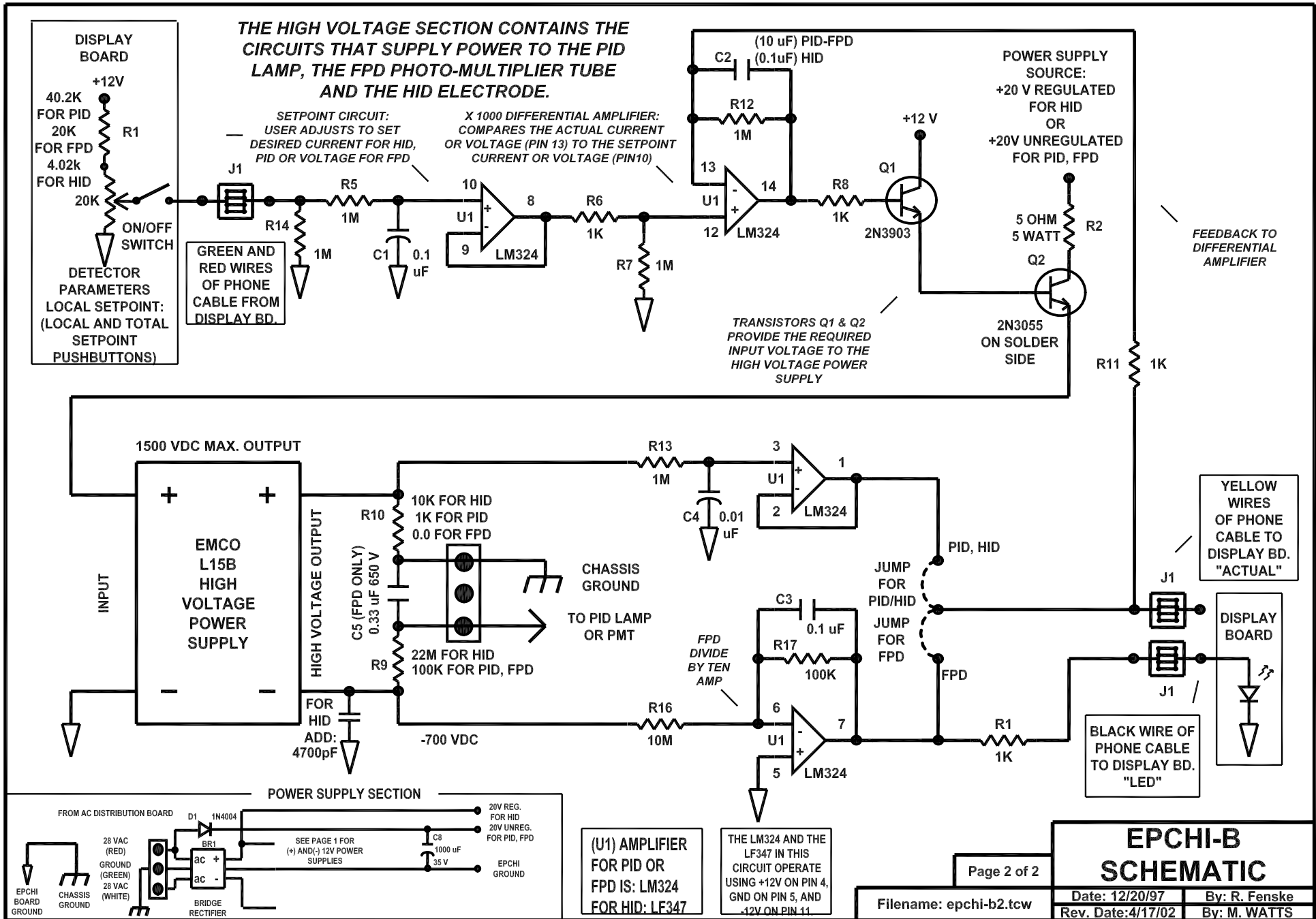
BLACK WIRE OF PHONE CABLE TO DISPLAY BD. "LED"

EPCH-B-EPC SCHEMATIC





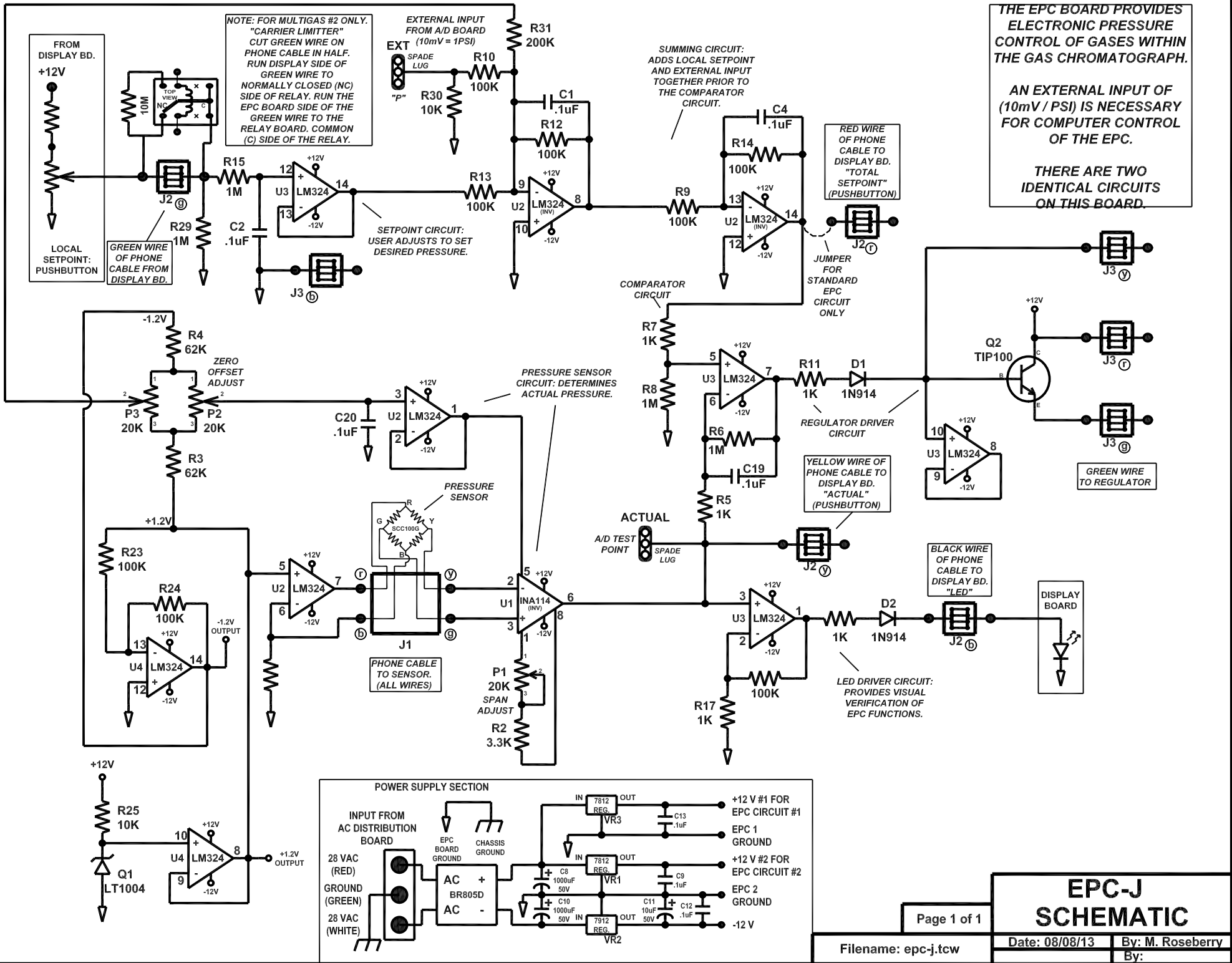


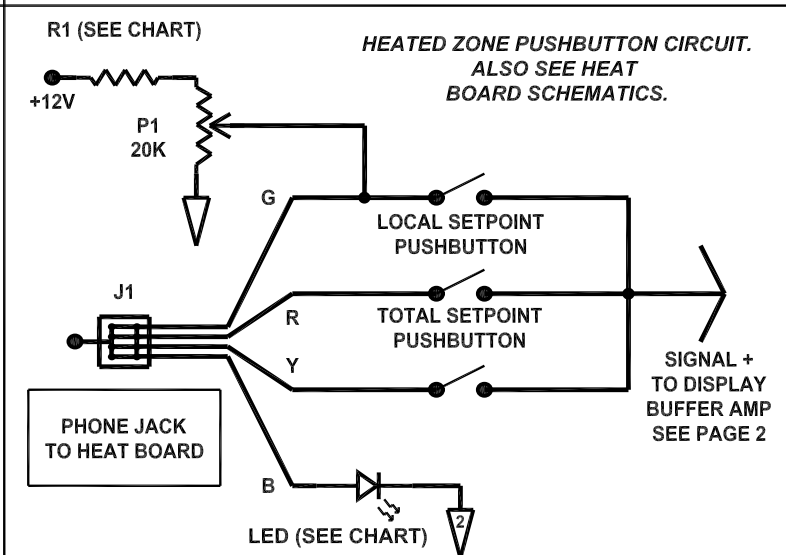
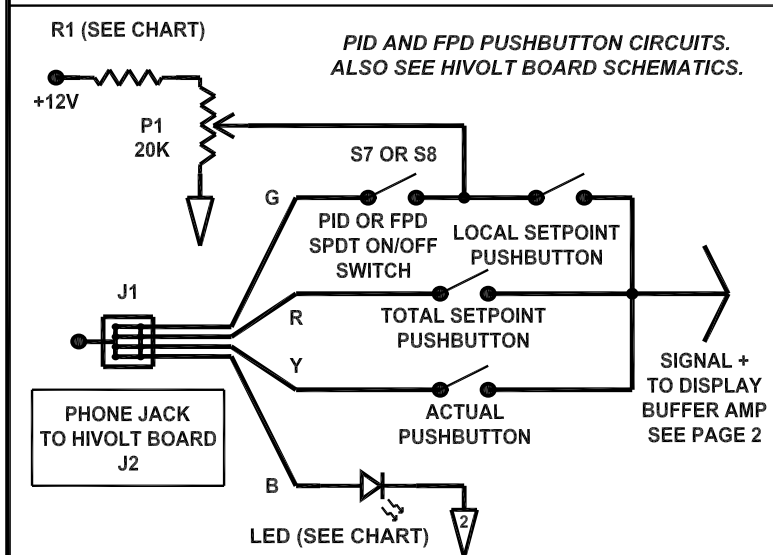
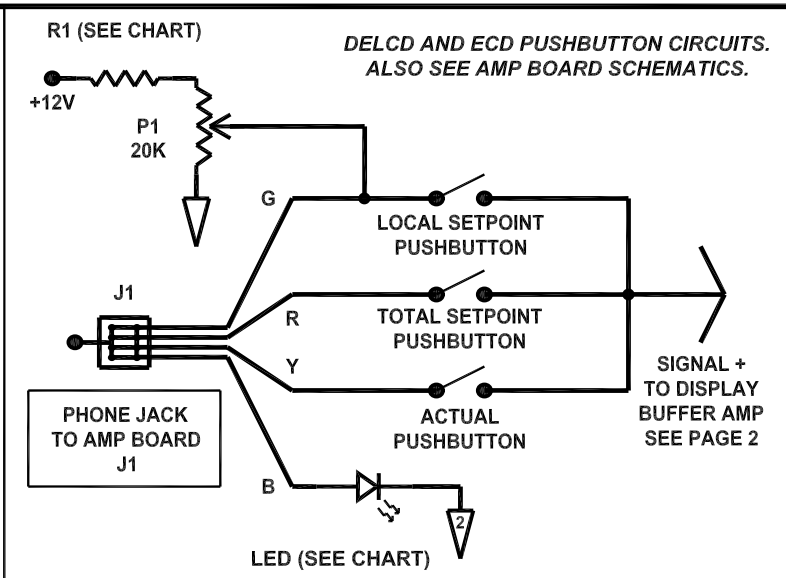
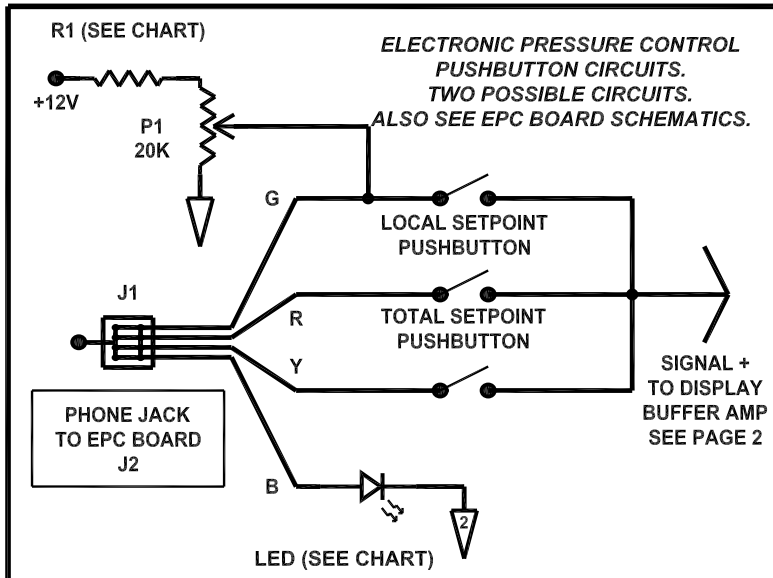


THE EPC BOARD PROVIDES ELECTRONIC PRESSURE CONTROL OF GASES WITHIN THE GAS CHROMATOGRAPH.

AN EXTERNAL INPUT OF (10mV / PSI) IS NECESSARY FOR COMPUTER CONTROL OF THE EPC.

THERE ARE TWO IDENTICAL CIRCUITS ON THIS BOARD.

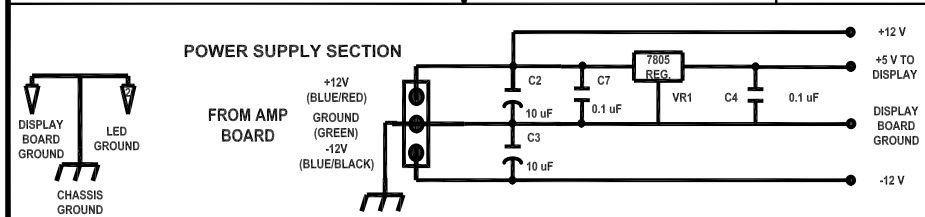




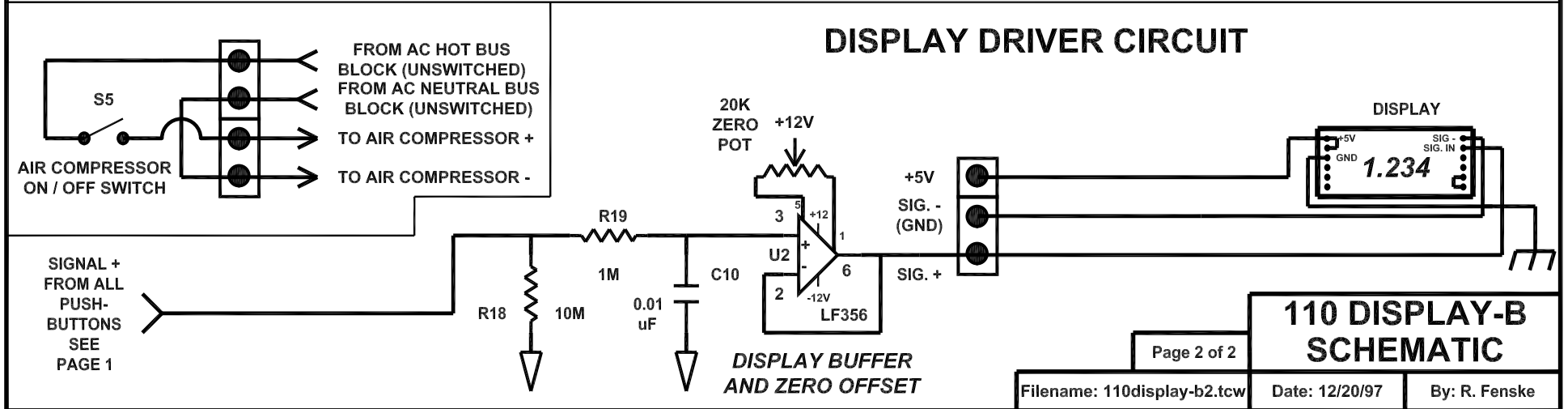
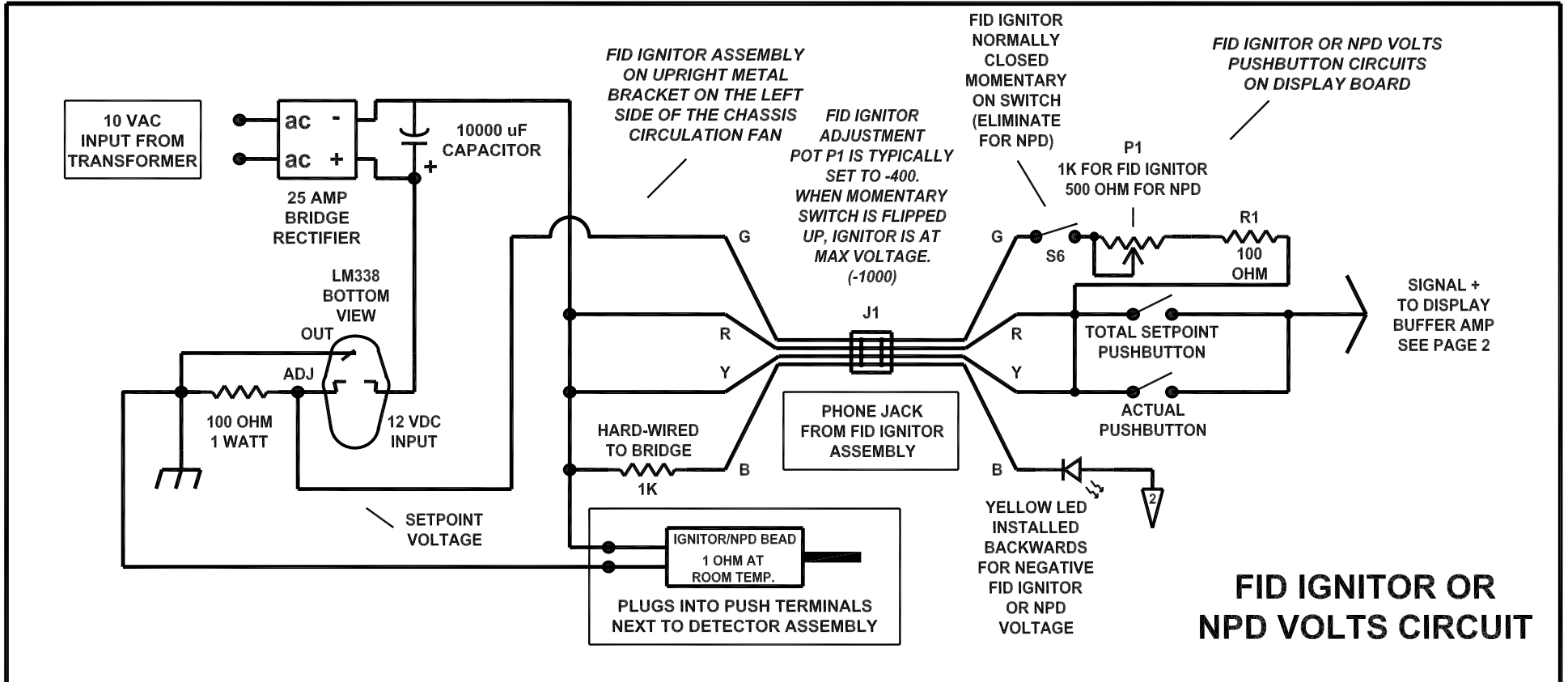
CHART

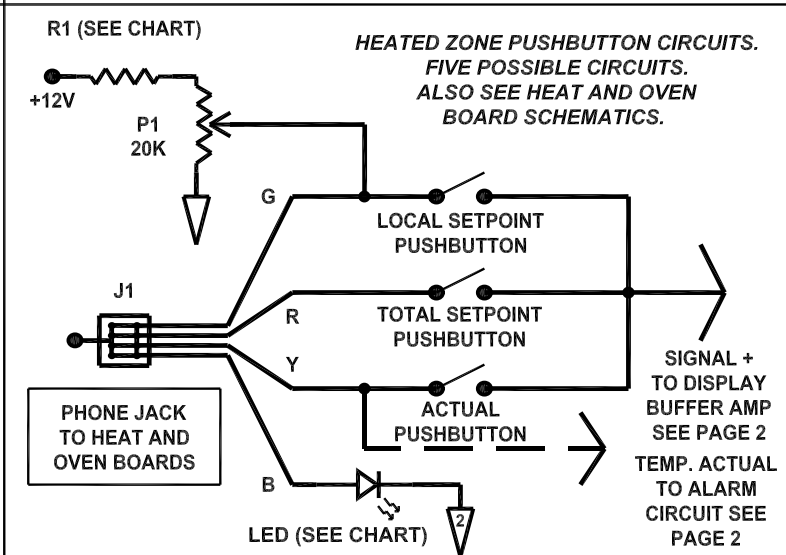
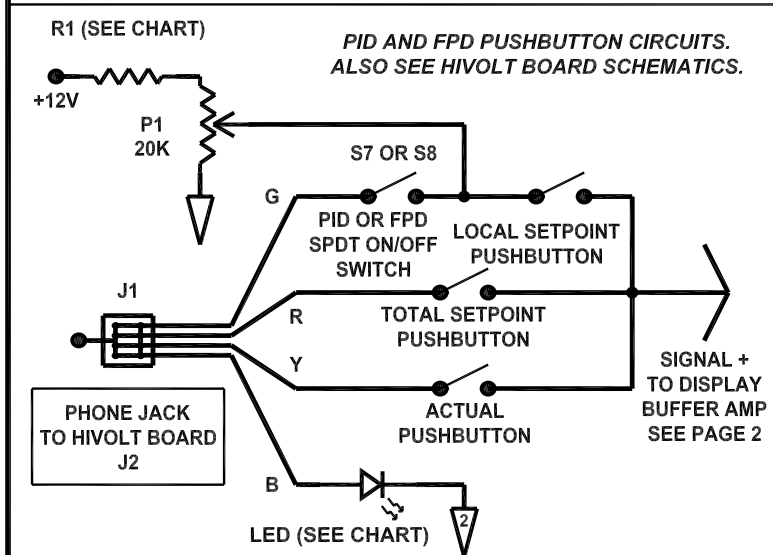
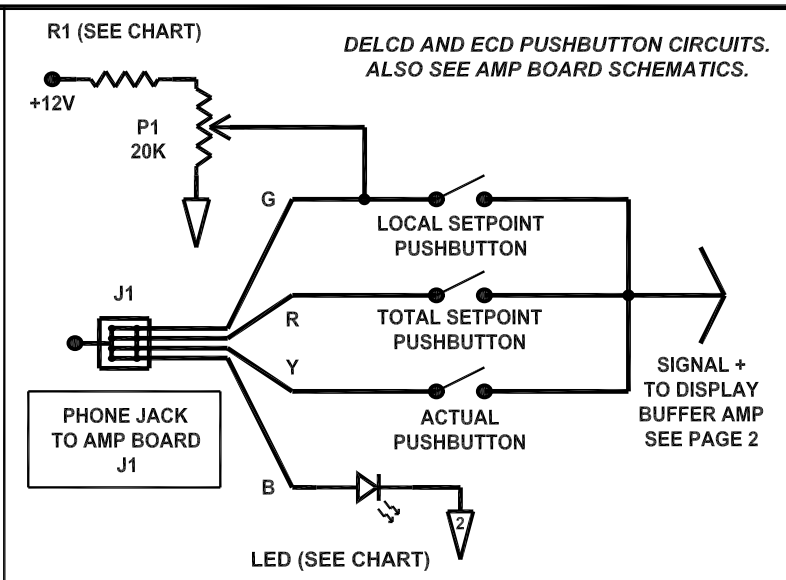
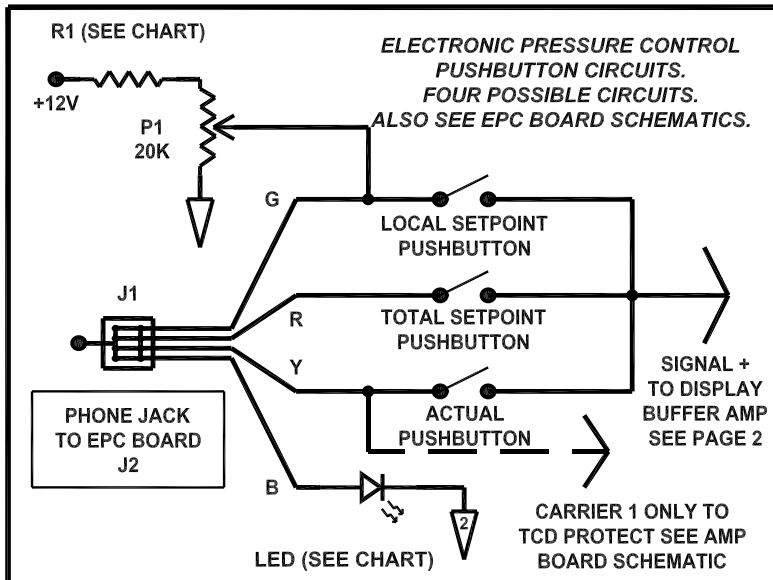
CIRCUIT	R1 VALUE	LED
HYDROGEN 1	220K	GREEN
AIR 1	1M	GREEN
FID IGNITOR	100	YELLOW
PID CURRENT	40.2K	YELLOW
FPD VOLTS	20K	YELLOW
ECD CURRENT	4.02K	YELLOW
DELCD REACT.	60.4K	YELLOW
FID BLOCK	40.2K	ORANGE
DELCD BLOCK	40.2K	ORANGE
PID BLOCK	75K	ORANGE
ECD BLOCK	44.2K	ORANGE
TCD BLOCK	120K	ORANGE

DISPLAY BOARD PUSHBUTTON CIRCUITS



THE LM324's ON THIS BOARD OPERATE USING +12V ON PIN 4 AND GROUND ON PIN 11.

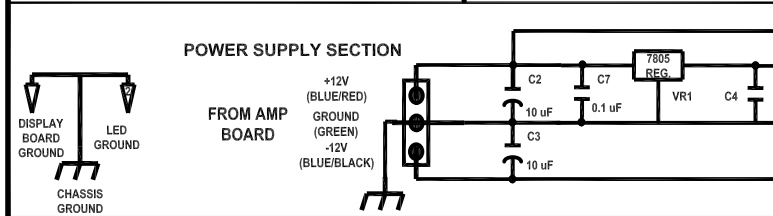




CHART

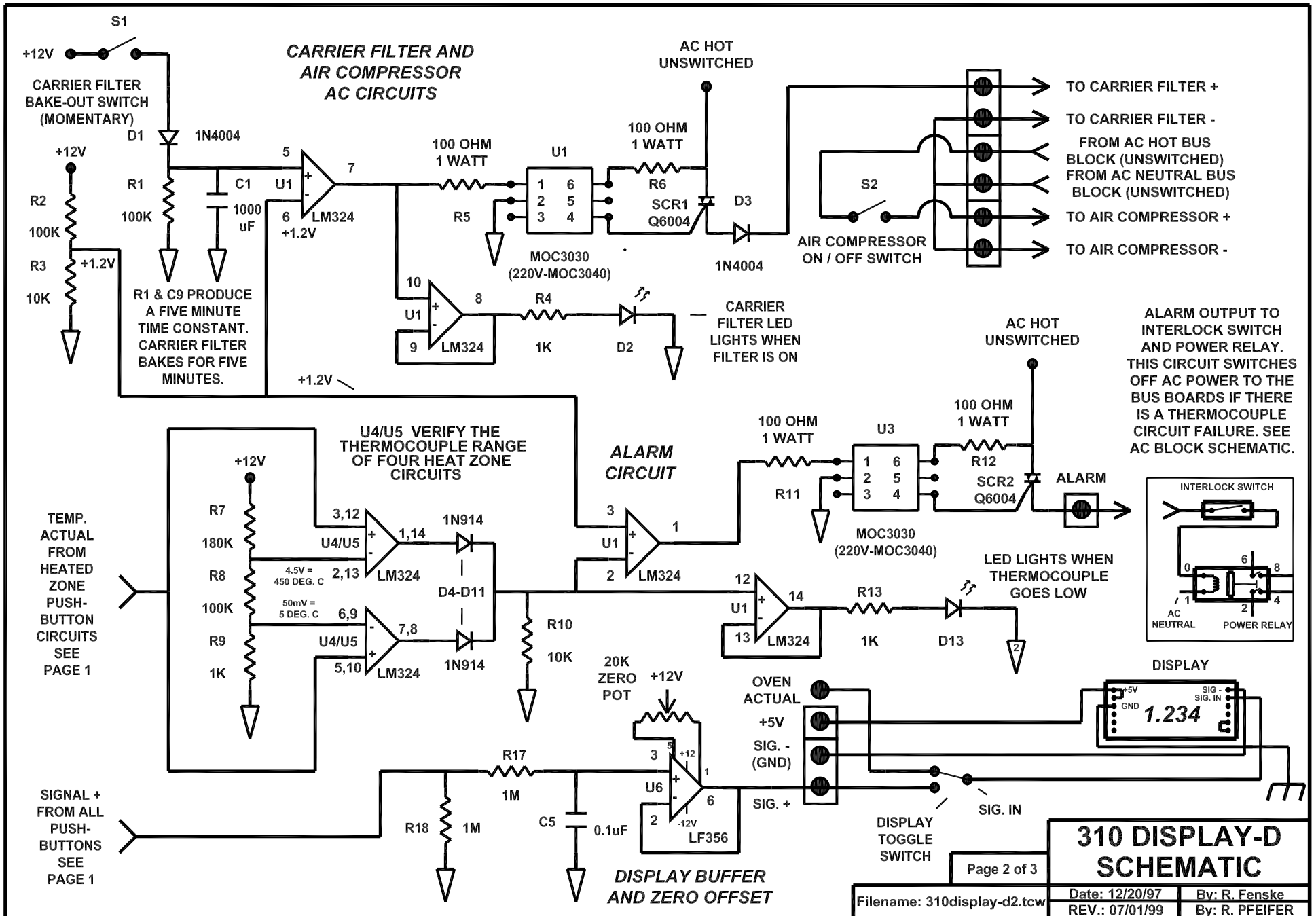
CIRCUIT	R1 VALUE	LED
CARRIER 1	220K	GREEN
HYDROGEN 1	220K	GREEN
AIR 1	1M	GREEN
AUXILIARY	220K	GREEN
TCD PROTECT	453K	RED
FID IGNITOR	100	YELLOW
PID CURRENT	40.2K	YELLOW
FPD VOLTS	20K	YELLOW
ECD CURRENT	4.02K	YELLOW
DELCD REACT.	60.4K	YELLOW
HEATED INJ. 1	60.4K	ORANGE
FID BLOCK	40.2K	ORANGE
DELCD BLOCK	40.2K	ORANGE
PID BLOCK	75K	ORANGE
ECD BLOCK	44.2K	ORANGE
TCD BLOCK	120K	ORANGE
COL. OVEN 1	40.2K	ORANGE
CHASSIS	383K	GREEN

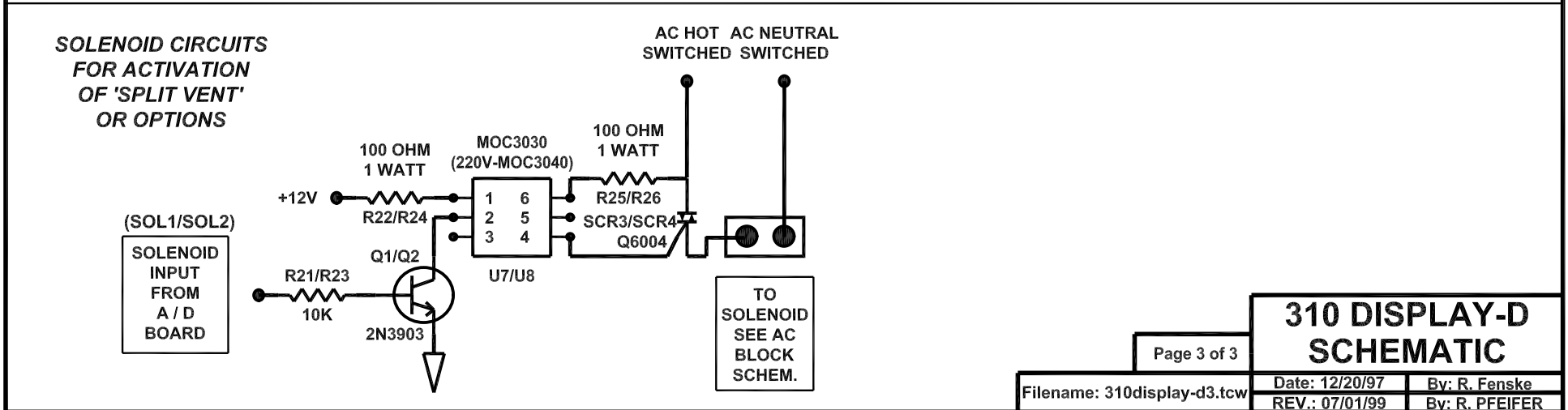
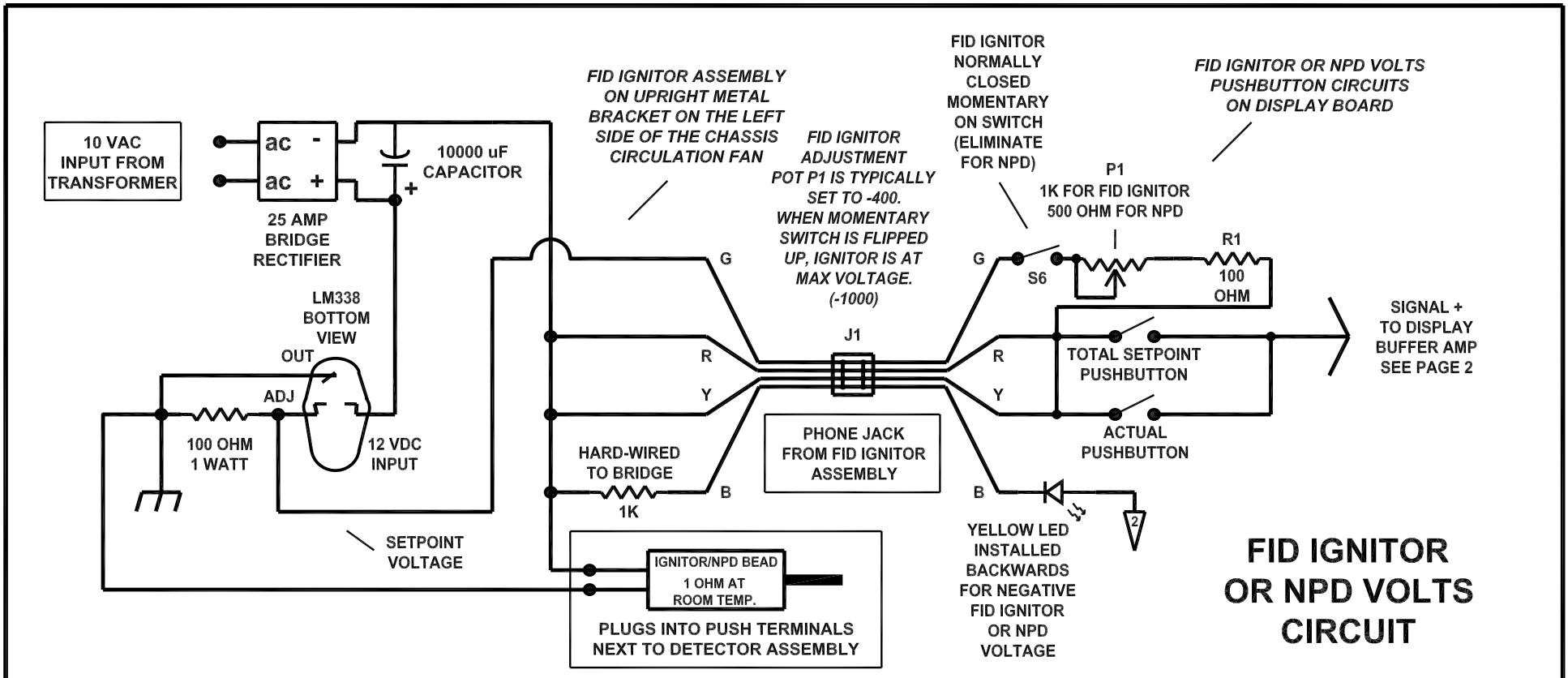
DISPLAY BOARD PUSHBUTTON CIRCUITS

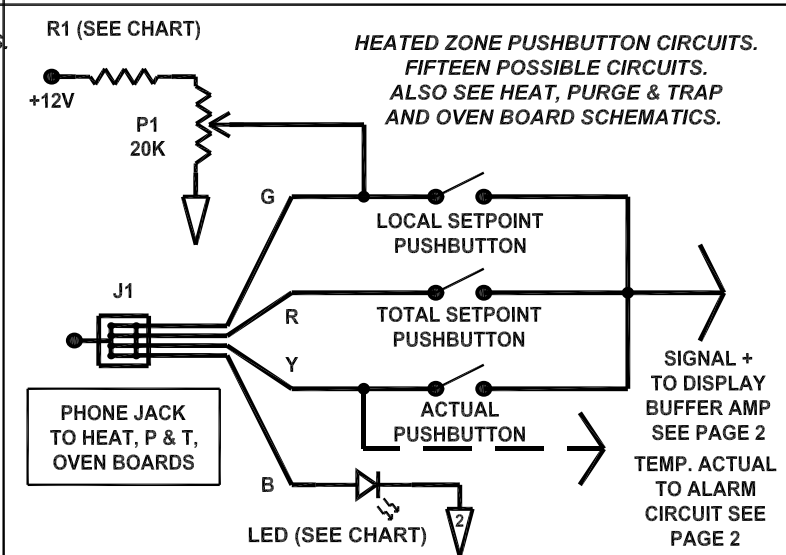
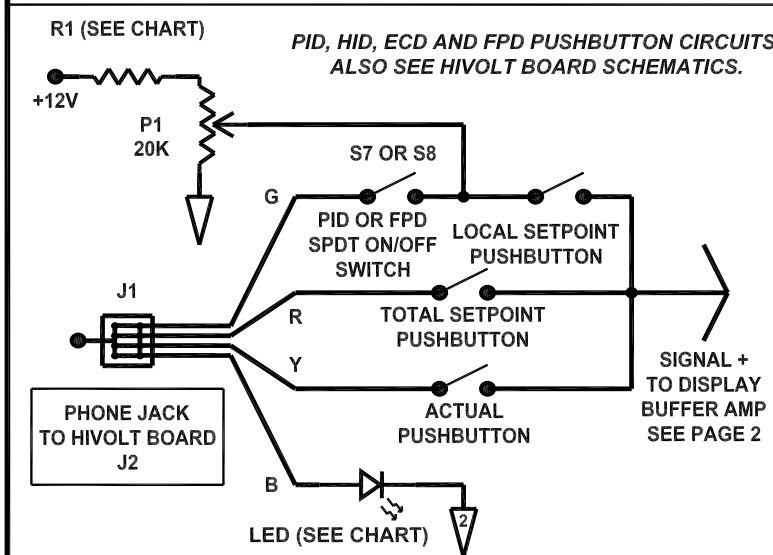
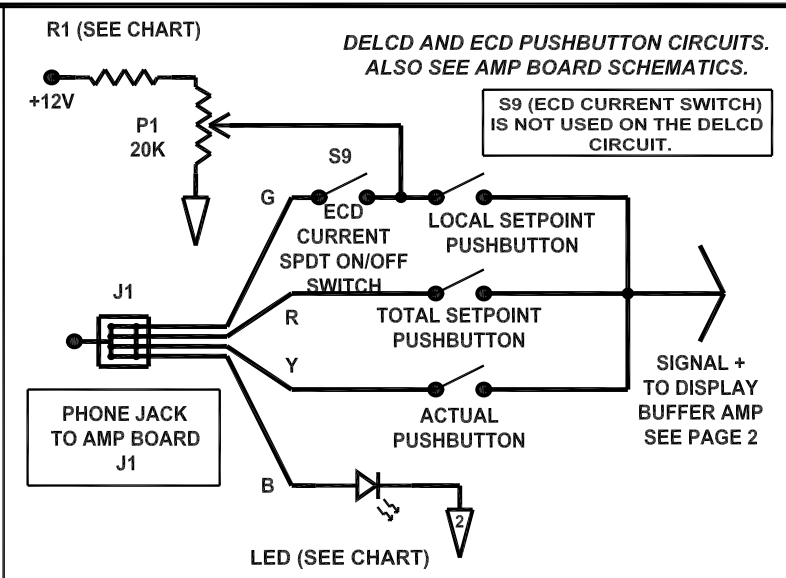
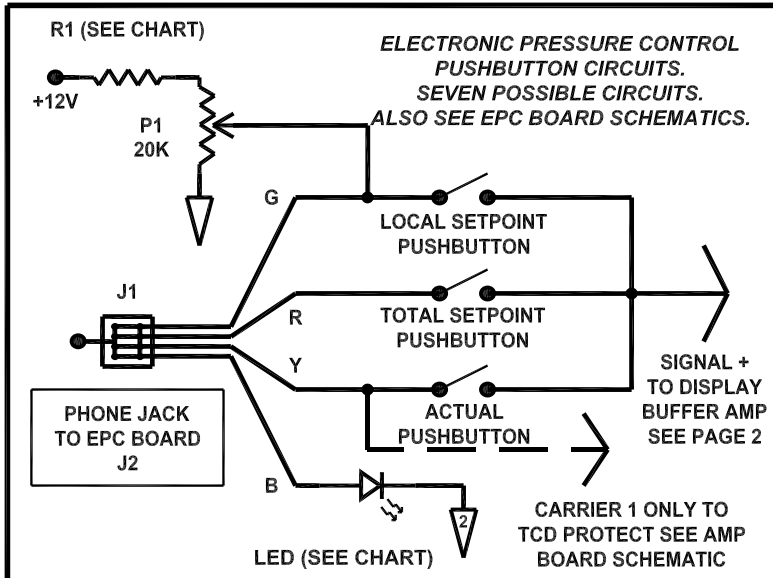


THE LM324's ON THIS BOARD OPERATE USING +12V ON PIN 4 AND GROUND ON PIN 11.

310 DISPLAY-D SCHEMATIC

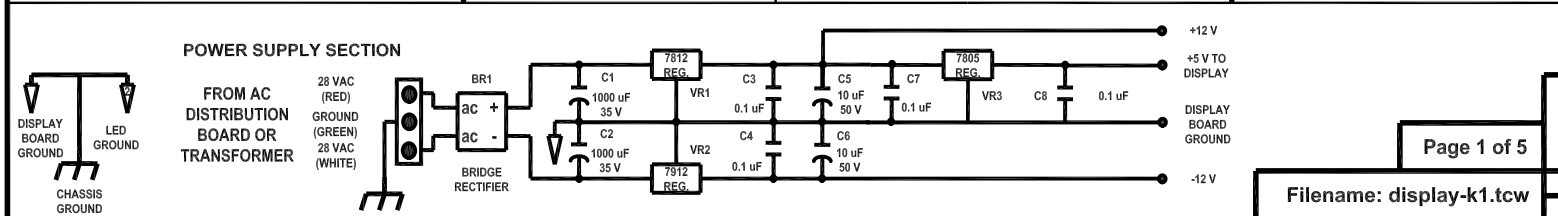






CHART

CIRCUIT	R1 VALUE	LED
CARRIER 1	220K	GREEN
CARRIER 2	220K	GREEN
PURGE	2.2M	GREEN
VIAL PRESSURE	220K	GREEN
HYDROGEN 1	220K	GREEN
HYDROGEN 2	220K	GREEN
AIR 1	1M	GREEN
AIR 2	1M	GREEN
BEAD VOLTS	620	YELLOW
REACTOR TEMP	48.7K	YELLOW
TCD PROTECT	453K	RED
VIRT. OVEN PROT.	390K	RED
FLAME IGNITE	100 ohm	YELLOW
PID CURRENT	40.2K	YELLOW
HID CURRENT	40.2K	YELLOW
PMT VOLTS	20K	YELLOW
ECD CURRENT	4.02K	YELLOW
TRAP 1	48.7K	ORANGE
TRAP 2	48.7K	ORANGE
VALVE 1	100K	ORANGE
VALVE 2	100K	ORANGE
VIAL	453K	ORANGE
HEADSPACE	220K	ORANGE
FID w/ METH.	38.3K	ORANGE
DESORBER	48.7K	ORANGE
INJECTOR 1	48.7K	ORANGE
INJECTOR 2	48.7K	ORANGE
FID BLOCK	38.3K	ORANGE
NPD BLOCK	40.2K	ORANGE
HID BLOCK	40.2K	ORANGE
FPD BLOCK	40.2K	ORANGE
TID BLOCK	40.2K	ORANGE
DELCD BLOCK	40.2K	ORANGE
PID BLOCK	75K	ORANGE
TCD CELL	48.7K	ORANGE
ECD BLOCK	44.2K	ORANGE
X-FER LINE	82K	ORANGE
COLUMN OVEN 1	40.2K	ORANGE
COLUMN OVEN 2	40.2K	ORANGE
OVEN MAX	38.3K	ORANGE

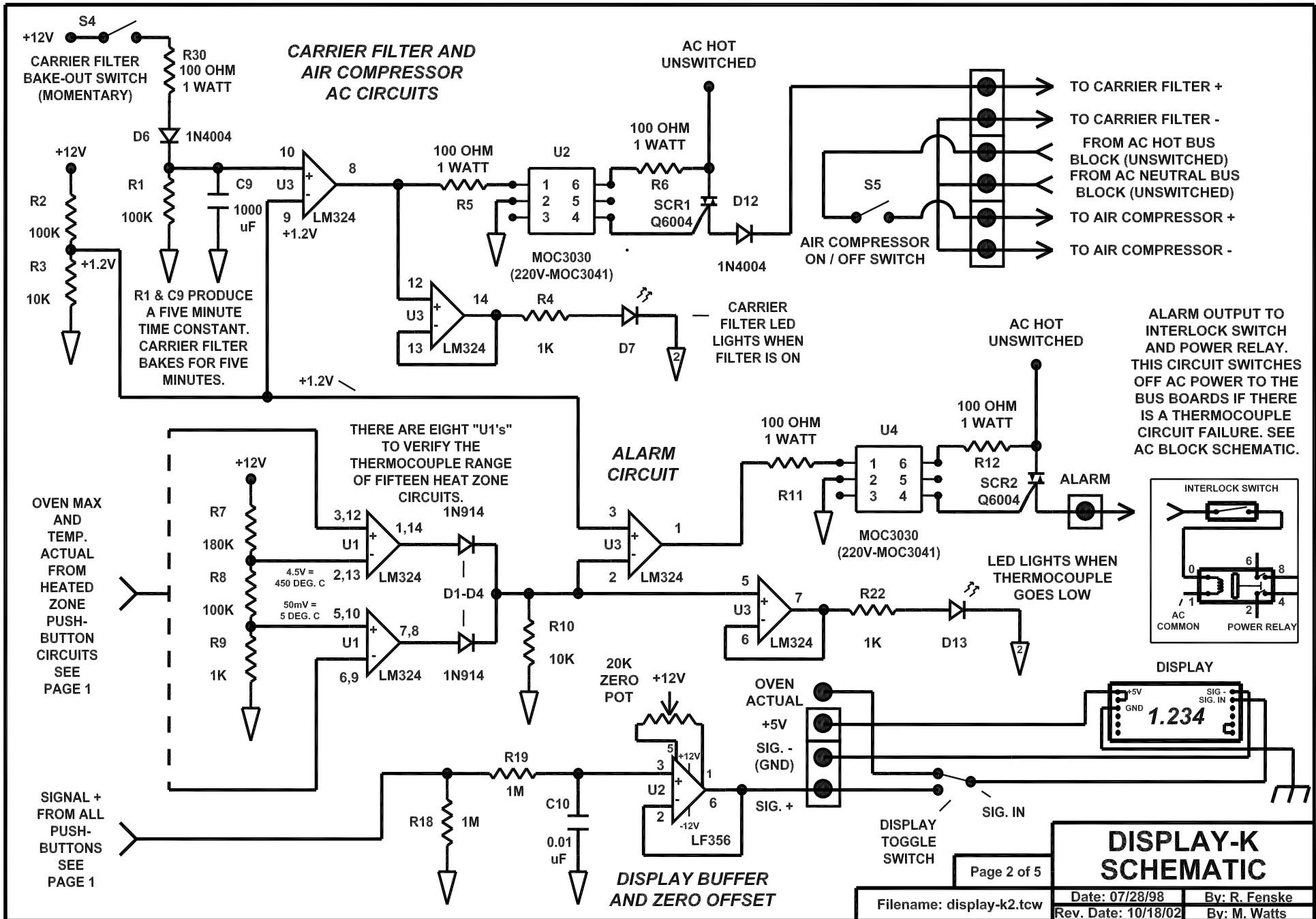


DISPLAY BOARD PUSHBUTTON CIRCUITS

8610 DISPLAY-K SCHEMATIC

Date: 12/20/97 By: R. Fenske

Rev. Date: 8/23/07 By: M. Roseberry



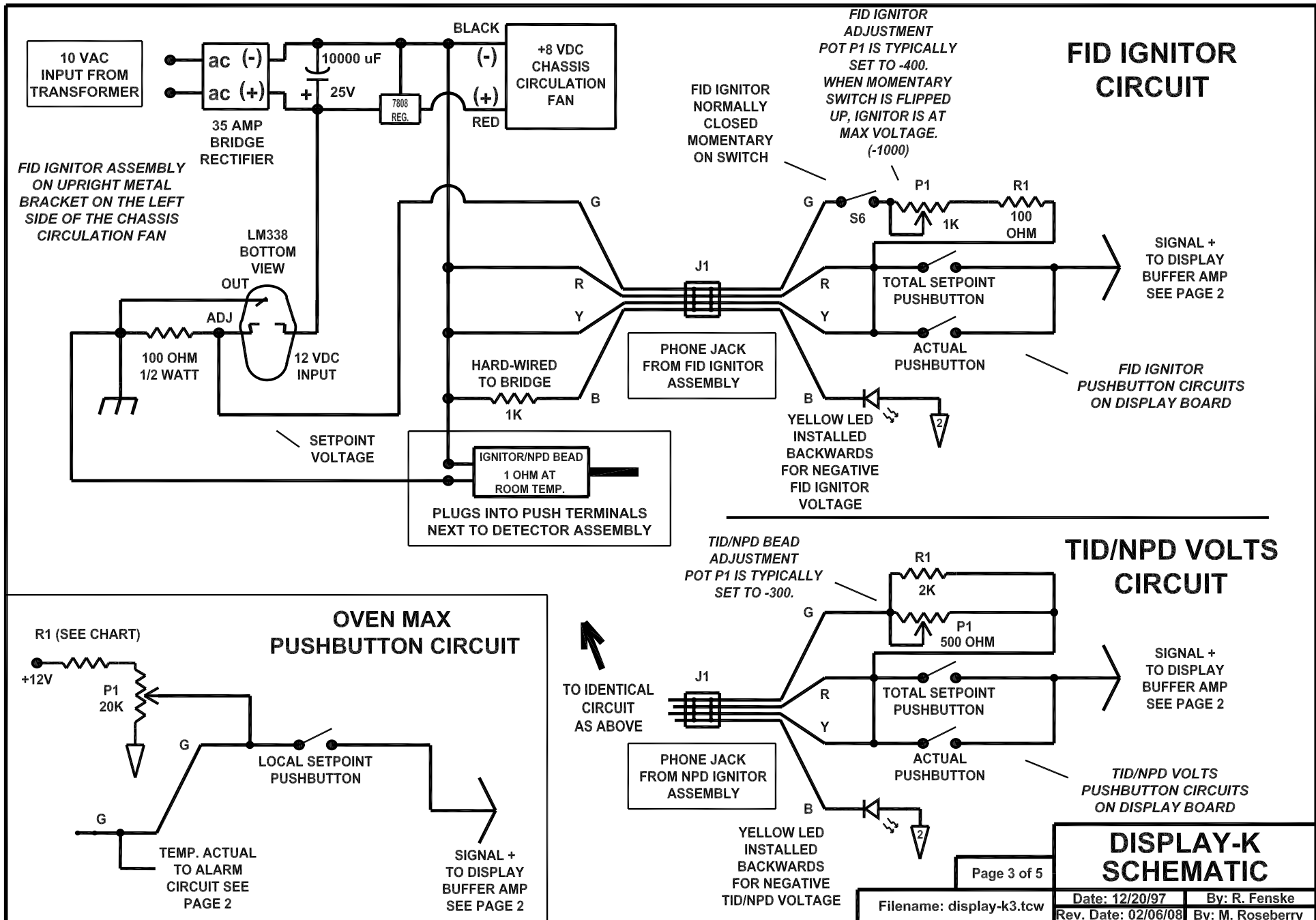
OVEN MAX AND TEMP. ACTUAL FROM HEATED ZONE PUSH-BUTTON CIRCUITS SEE PAGE 1

SIGNAL + FROM ALL PUSH-BUTTONS SEE PAGE 1

THERE ARE EIGHT "U1's" TO VERIFY THE THERMOCOUPLE RANGE OF FIFTEEN HEAT ZONE CIRCUITS.

ALARM CIRCUIT

DISPLAY BUFFER AND ZERO OFFSET

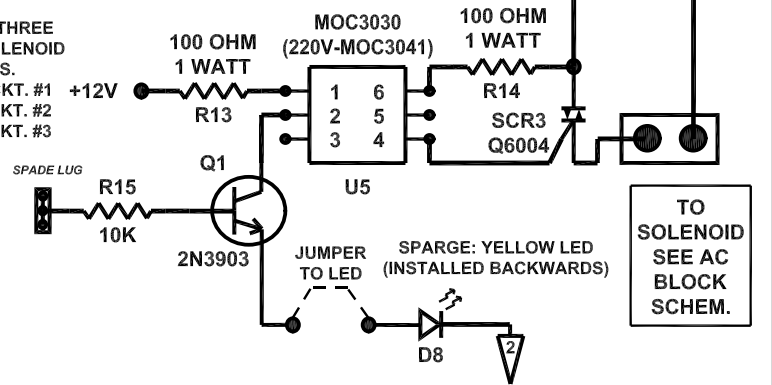


**SOLENOID CIRCUIT FOR ACTIVATION OF
'SPARGE', 'SPLIT VENT', 'CRYO TRAP'**

SOLENOID CKT. #1

THERE ARE THREE IDENTICAL SOLENOID CIRCUITS.
SOLENOID CKT. #1
SOLENOID CKT. #2
SOLENOID CKT. #3

SOLENOID INPUT FROM A/D BOARD

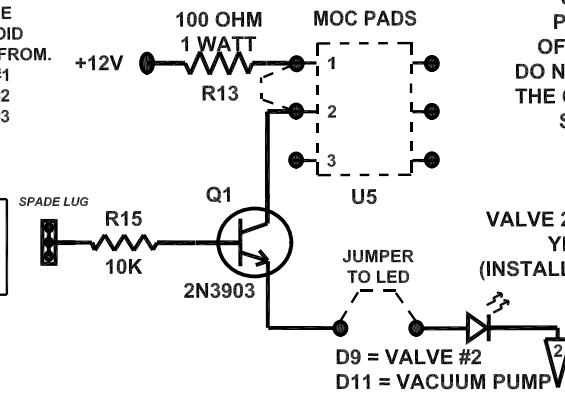


TO SOLENOID SEE AC BLOCK SCHEM.

TO LIGHT THE VALVE OR VACUUM PUMP LED(S) ONLY, THIS SECTION OF ANY OF THE THREE SOLENOID CIRCUITS IS USED.

THERE ARE THREE IDENTICAL SOLENOID CIRCUITS TO SELECT FROM.
SOLENOID CKT. #1
SOLENOID CKT. #2
SOLENOID CKT. #3

VIOLET OR GRAY FROM A/D BOARD



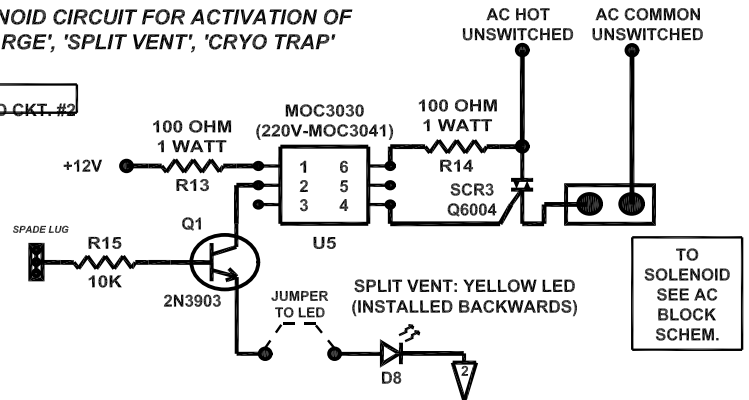
JUMPER PINS 1 & 2 OF THE MOC. DO NOT INSTALL THE CHIP OR THE SOCKET.

VALVE 2, VACUUM PUMP: YELLOW LED (INSTALLED BACKWARDS)

**SOLENOID CIRCUIT FOR ACTIVATION OF
'SPARGE', 'SPLIT VENT', 'CRYO TRAP'**

SOLENOID CKT. #2

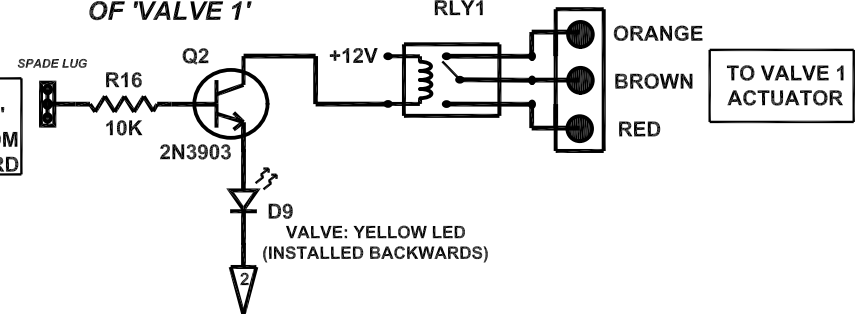
SOLENOID INPUT FROM A/D BOARD



TO SOLENOID SEE AC BLOCK SCHEM.

RELAY CIRCUIT FOR ACTIVATION OF 'VALVE 1'

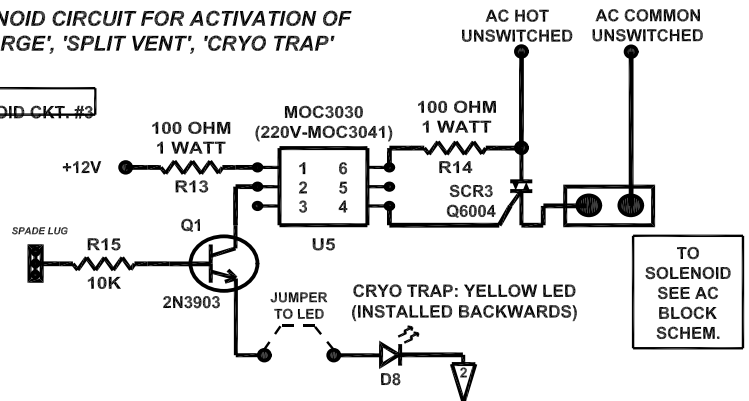
VALVE 1 RELAY 'G' INPUT FROM A/D BOARD



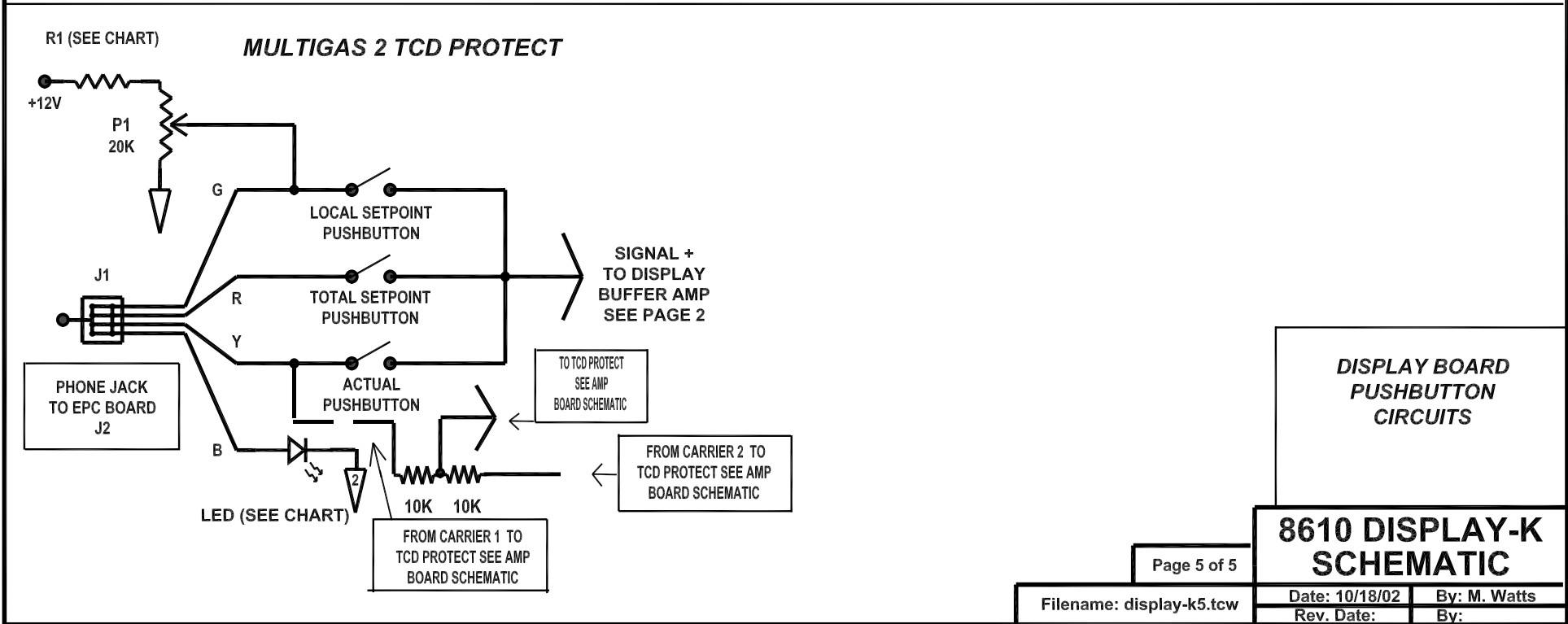
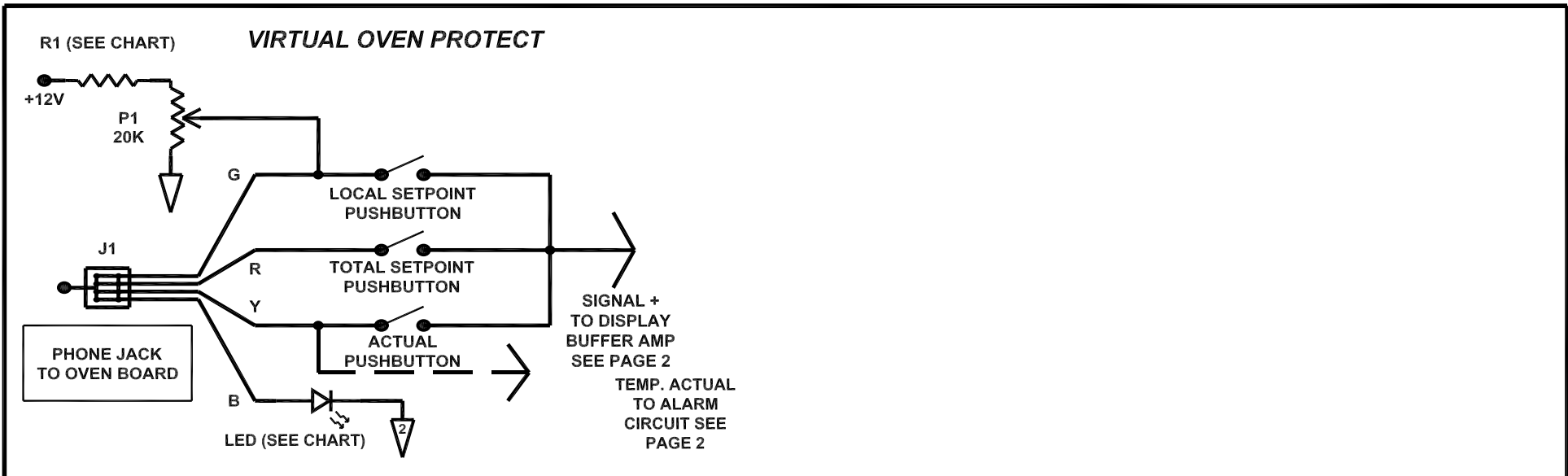
**SOLENOID CIRCUIT FOR ACTIVATION OF
'SPARGE', 'SPLIT VENT', 'CRYO TRAP'**

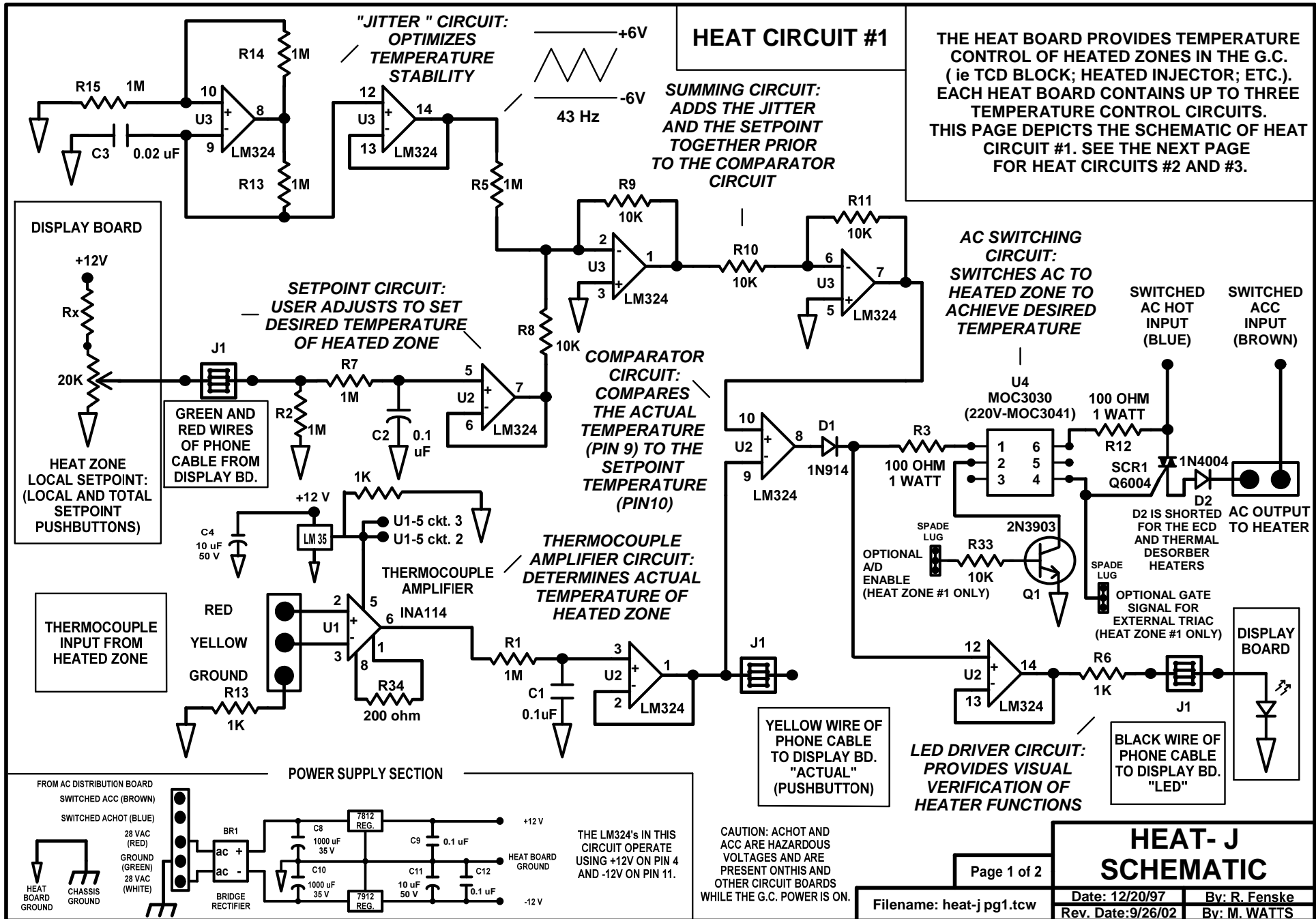
SOLENOID CKT. #3

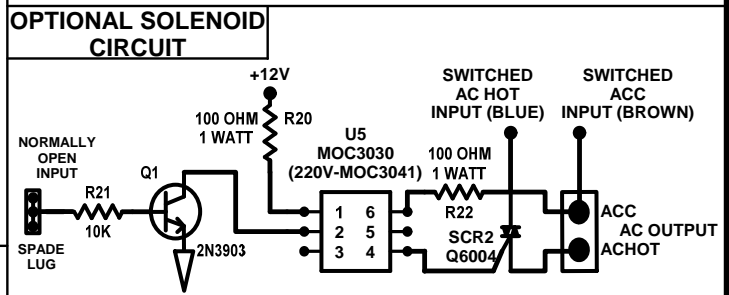
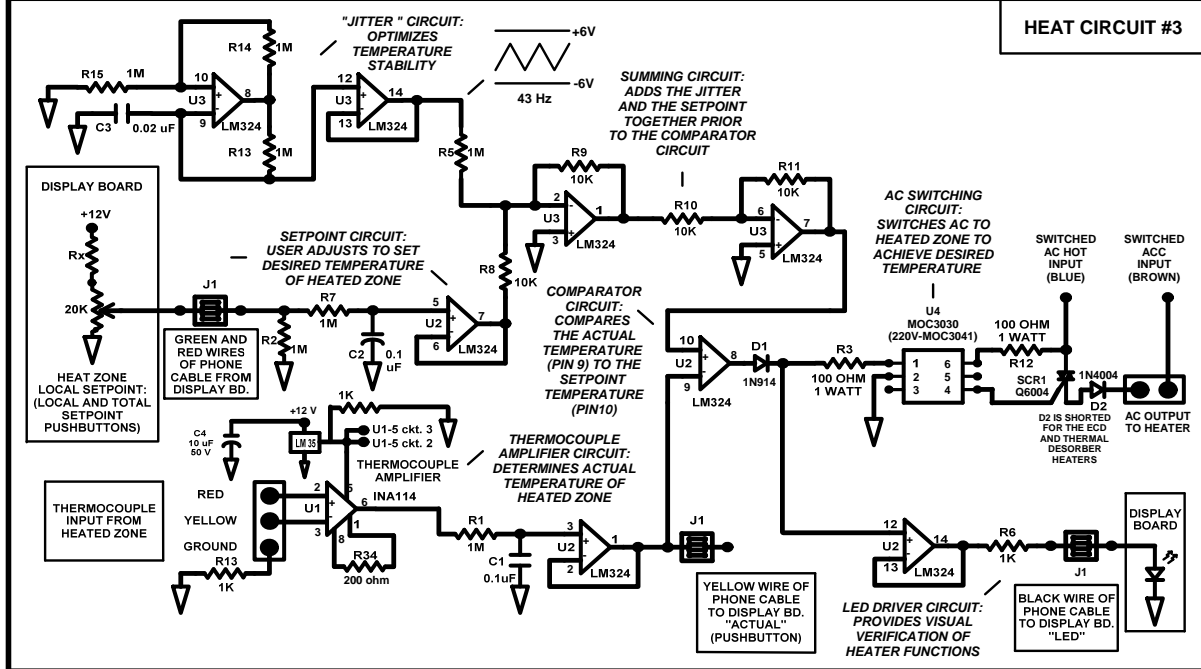
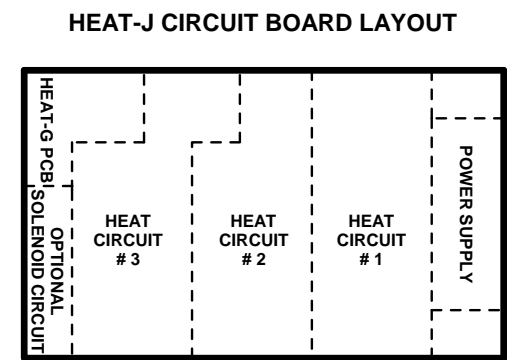
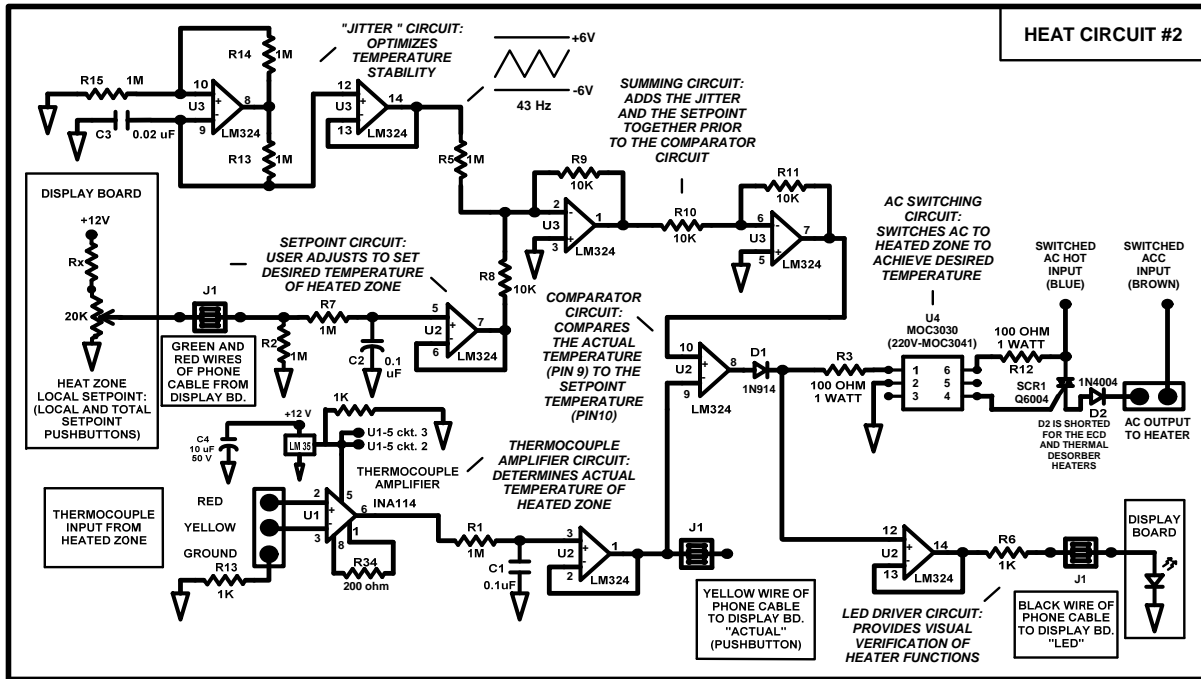
SOLENOID INPUT FROM A/D BOARD



TO SOLENOID SEE AC BLOCK SCHEM.



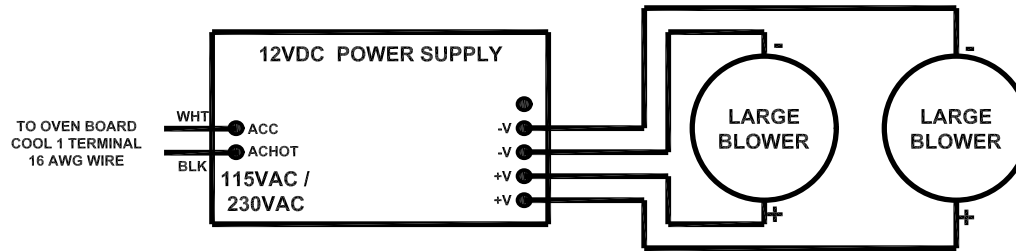




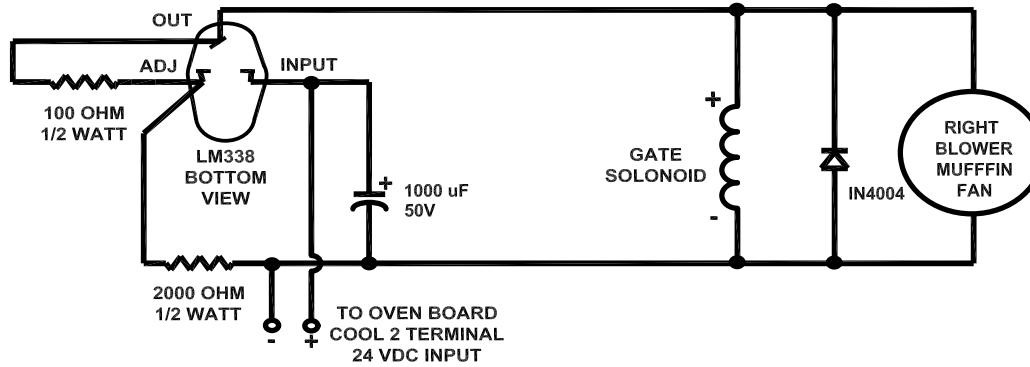
THIS PAGE DEPICTS THE SCHEMATICS OF HEAT CIRCUITS #2 AND #3 AND OF THE OPTIONAL SOLENOID CIRCUIT. THE DIFFERENCES BETWEEN HEAT CIRCUITS #1, #2 AND #3 ARE:

- CIRCUIT #1 HAS AN OPTIONAL A/D ENABLE OF THE HEAT ZONE AND AN OPTIONAL GATE SIGNAL INPUT FOR AN EXTERNAL TRIAC.
- ALSO, THE SUMMING CIRCUIT IN EACH HAVE DIFFERENT OP-AMP PINOUTS:
- HEAT CIRCUIT #1 USES U3 PIN 1 AND PIN 7 OUTPUTS.
- HEAT CIRCUIT #2 USES U5 PIN 1 AND PIN 7 OUTPUTS.
- HEAT CIRCUIT #3 USES U5 PIN 14 AND PIN 8 OUTPUTS.

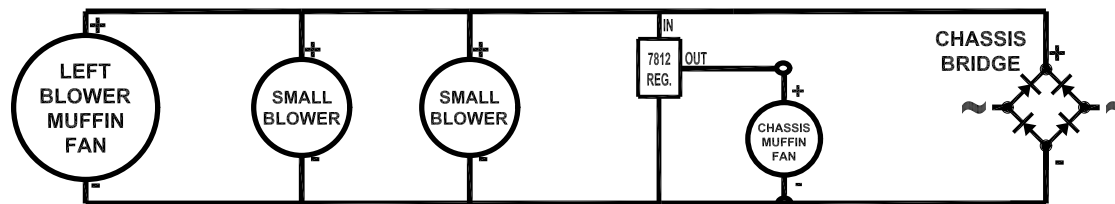
VERTICAL OVEN COOL DOWN CIRCUIT



GATE SOLONOID CIRCUIT



CIRCULATING FANS CIRCUIT



OVEN COOL DOWN & GATE SCHEMATIC

Page 1 of 1

Filename: Oven Cool Down_Gate Circuit.tcx

11/22/11

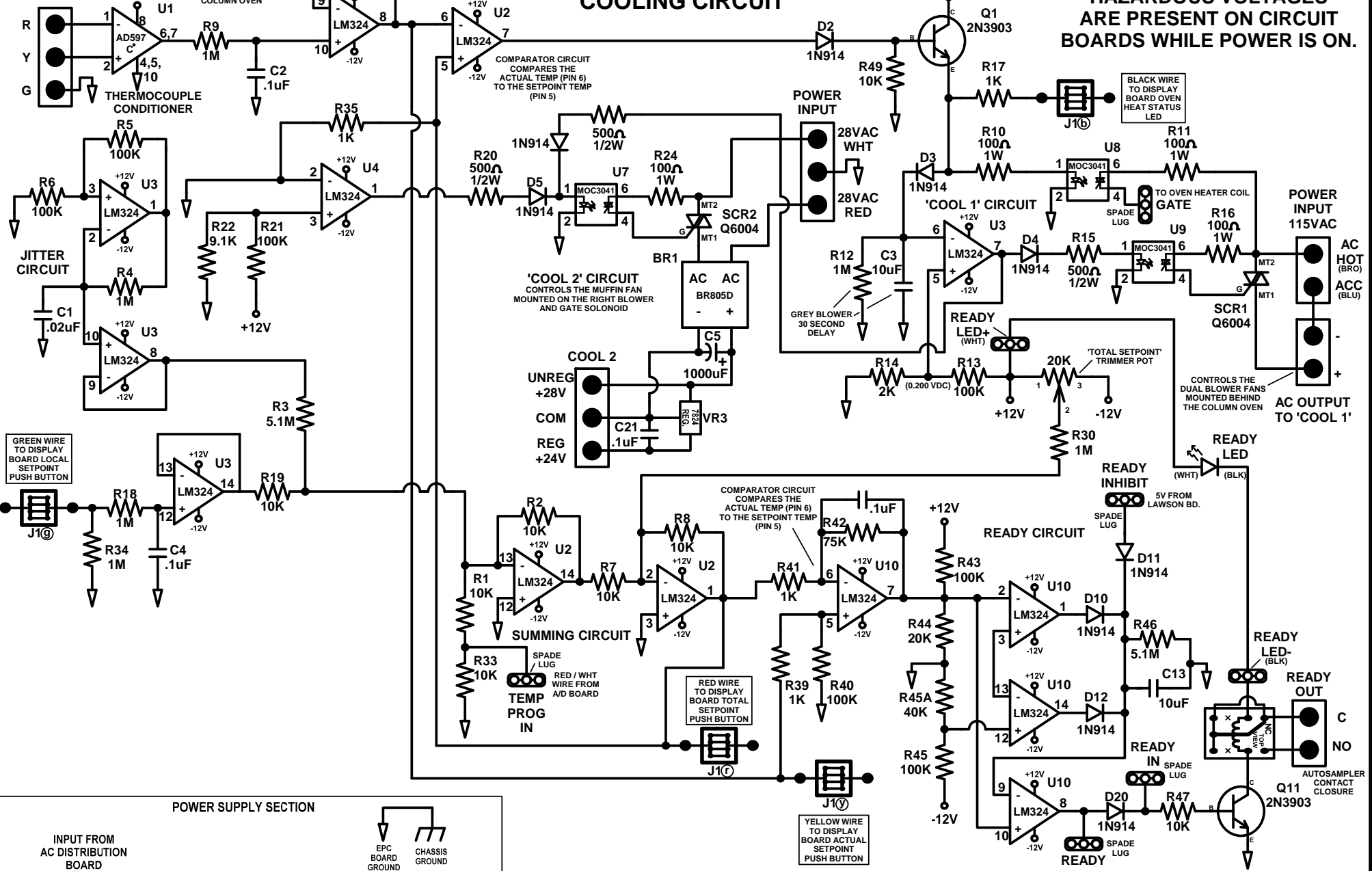
Rev. Date:

By: M. Roseberry

VERTICAL OVEN HEATING AND COOLING CIRCUIT

VERTICAL OVEN HEATING AND COOLING CIRCUIT

CAUTION:
HAZARDOUS VOLTAGES
ARE PRESENT ON CIRCUIT
BOARDS WHILE POWER IS ON.



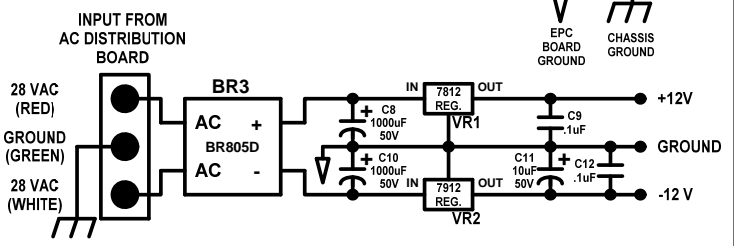
GREEN WIRE TO DISPLAY BOARD LOCAL SETPOINT PUSH BUTTON

BLACK WIRE TO DISPLAY BOARD OVEN HEAT STATUS LED

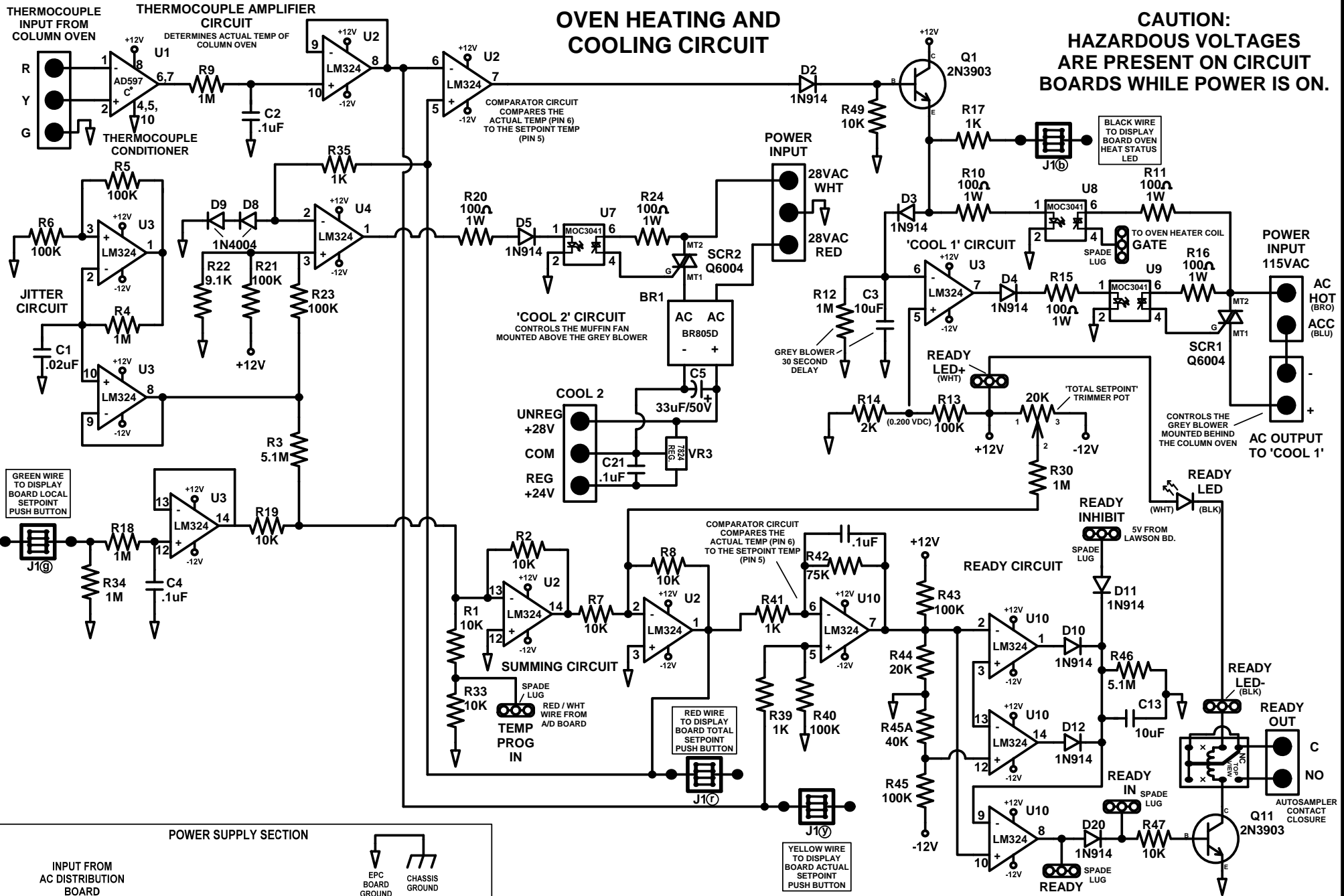
YELLOW WIRE TO DISPLAY BOARD ACTUAL SETPOINT PUSH BUTTON

CONTROLS THE DUAL BLOWER FANS MOUNTED BEHIND THE COLUMN OVEN

POWER SUPPLY SECTION



OVEN-I Vertical Mod SCHEMATIC



**CAUTION:
HAZARDOUS VOLTAGES
ARE PRESENT ON CIRCUIT
BOARDS WHILE POWER IS ON.**

**OVEN HEATING AND
COOLING CIRCUIT**

**THERMOCOUPLE AMPLIFIER
CIRCUIT**

THERMOCOUPLE
INPUT FROM
COLUMN OVEN

DETERMINES ACTUAL TEMP OF
COLUMN OVEN

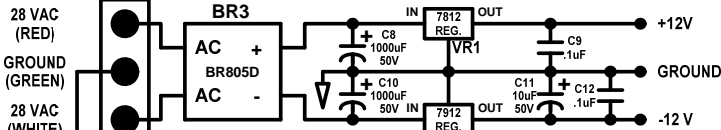
COMPARATOR CIRCUIT
COMPARES THE
ACTUAL TEMP (PIN 6)
TO THE SETPOINT TEMP
(PIN 5)

'COOL 2' CIRCUIT
CONTROLS THE MUFFIN FAN
MOUNTED ABOVE THE GREY BLOWER

COMPARATOR CIRCUIT
COMPARES THE
ACTUAL TEMP (PIN 6)
TO THE SETPOINT TEMP
(PIN 5)

POWER SUPPLY SECTION

INPUT FROM
AC DISTRIBUTION
BOARD

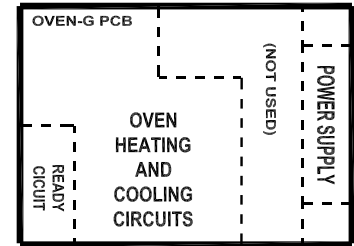


**OVEN-I
SCHEMATIC**

THE DUAL OVEN BOARD #2 PROVIDES CONTROL OF OVEN HEATING AND COOLING. THIS PAGE DEPICTS THE POWER SUPPLY CIRCUIT AND JITTER CIRCUIT.
SEE PAGE TWO FOR OVEN HEATING AND COOLING CIRCUITS.

CHASSIS COOLING FAN CIRCUIT (NOT USED)

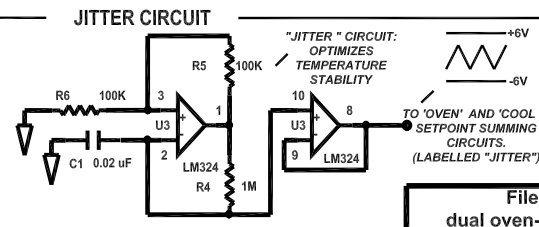
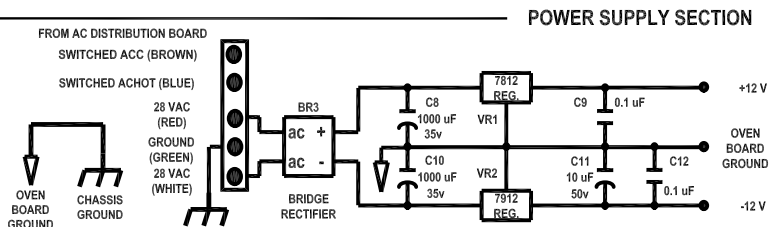
OVEN-G CIRCUIT BOARD LAYOUT



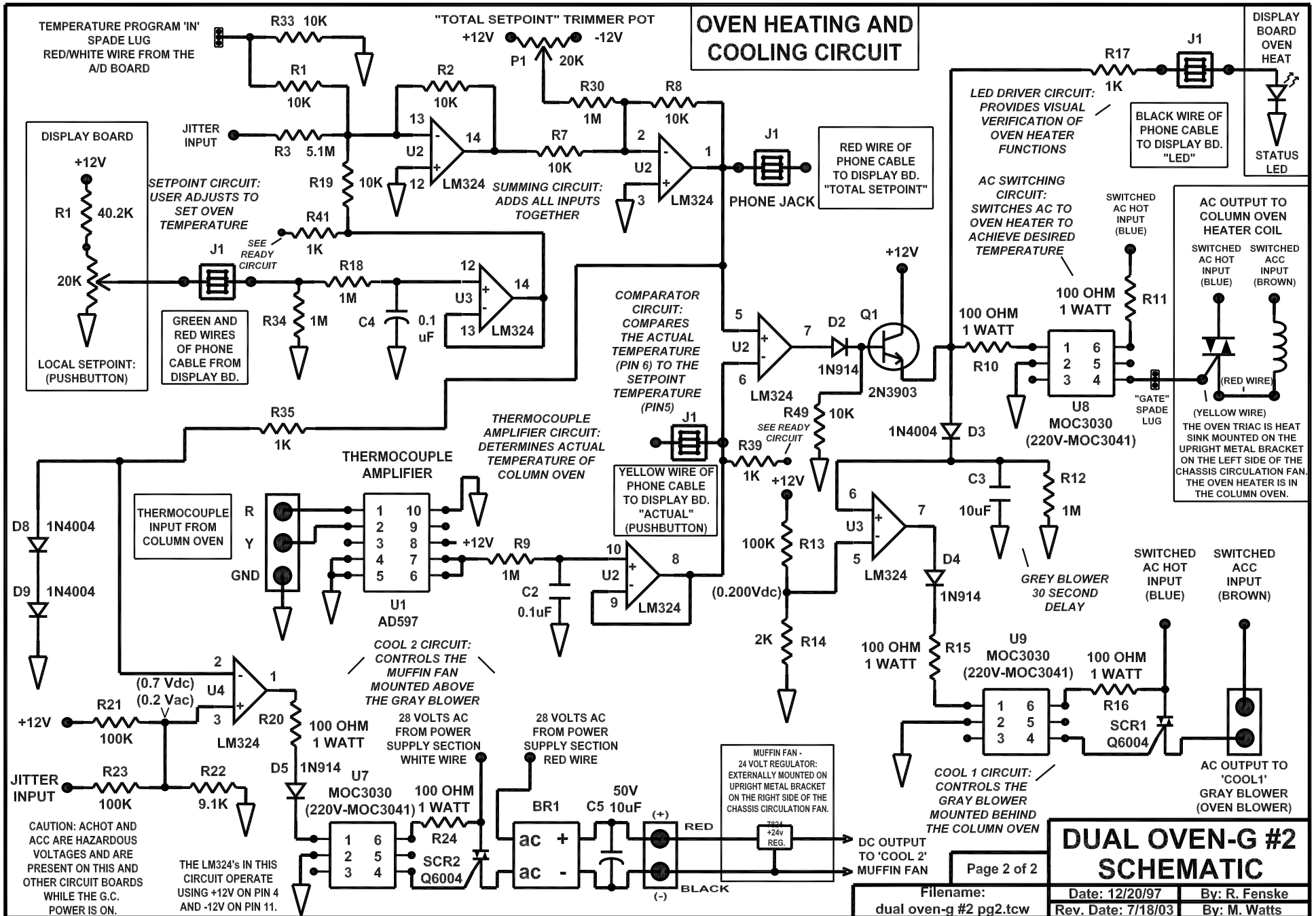
DUAL OVEN CONFIGURATION REQUIRES THE USE OF A STANDARD OVEN PCB FOR OVEN #1 AND A SECOND OVEN PCB WITHOUT THE CHASSIS COOLING FAN CIRCUIT FOR OVEN #2. IF NECESSARY, A STANDARD OVEN BOARD MAYBE USED FOR OVEN #2, PROVIDED THAT THE CHASSIS COOLING CIRCUIT IS LEFT UN-USED. OVEN-F #2 IS THE SCHEMATIC DIAGRAM FOR THE OVEN BOARD WITHOUT THE CHASSIS COOLING CIRCUIT; AS USED FOR THE SECOND OVEN IN A DUAL OVEN CONFIGURATION.

THE LM324's IN THIS CIRCUIT OPERATE USING +12V ON PIN 4 AND -12V ON PIN 11.

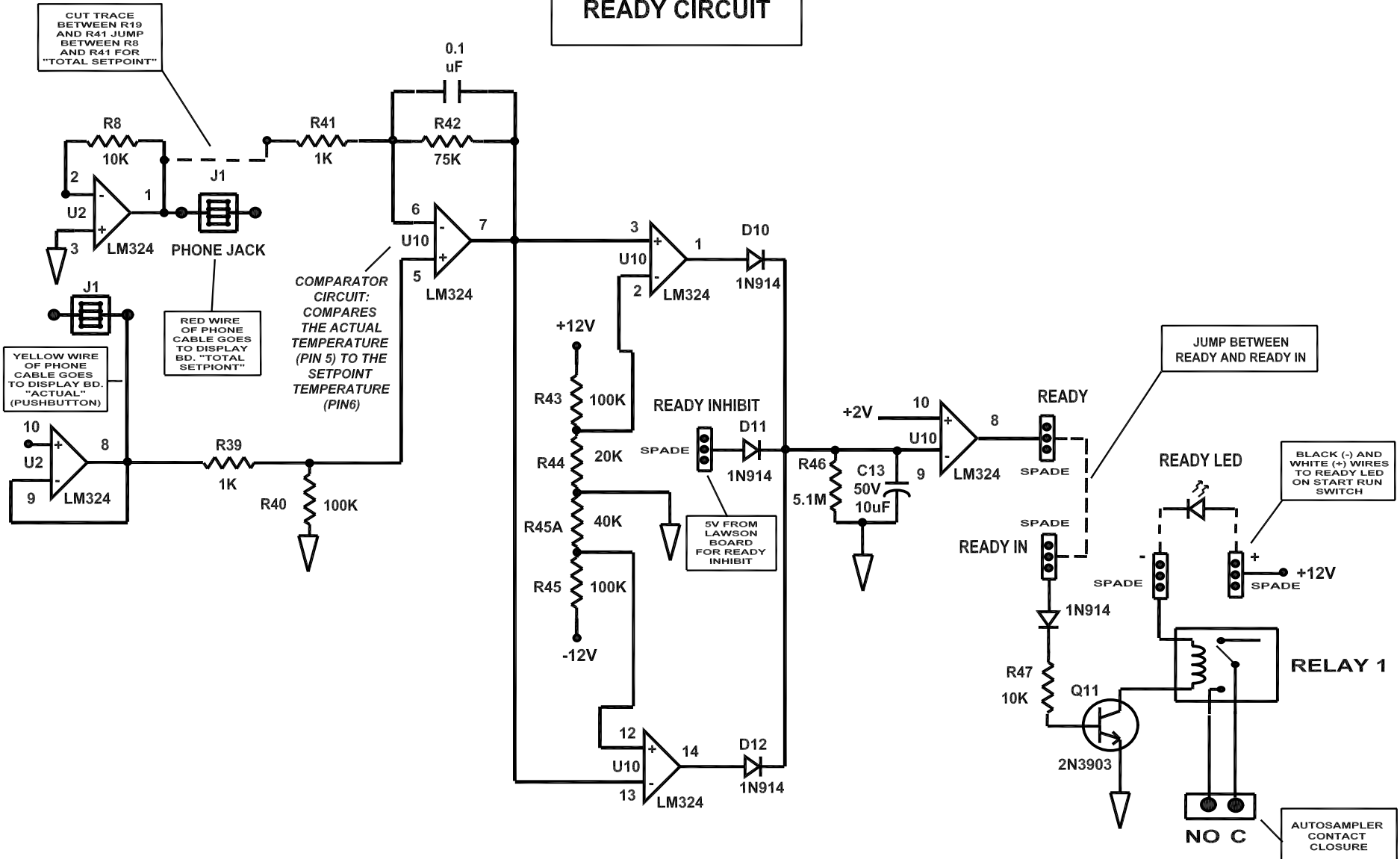
CAUTION: ACHOT AND ACC ARE HAZARDOUS VOLTAGES AND ARE PRESENT ON THIS AND OTHER CIRCUIT BOARDS WHILE THE G.C. POWER IS ON.



DUAL OVEN-G #2 SCHEMATIC



READY CIRCUIT



CUT TRACE BETWEEN R19 AND R41 JUMP BETWEEN R8 AND R41 FOR "TOTAL SETPOINT"

YELLOW WIRE OF PHONE CABLE GOES TO DISPLAY BD. "ACTUAL" (PUSHBUTTON)

RED WIRE OF PHONE CABLE GOES TO DISPLAY BD. "TOTAL SETPOINT"

COMPARATOR CIRCUIT: COMPARES THE ACTUAL TEMPERATURE (PIN 5) TO THE SETPOINT TEMPERATURE (PIN6)

5V FROM LAWSON BOARD FOR READY INHIBIT

JUMP BETWEEN READY AND READY IN

BLACK (-) AND WHITE (+) WIRES TO READY LED ON START RUN SWITCH

AUTOSAMPLER CONTACT CLOSURE

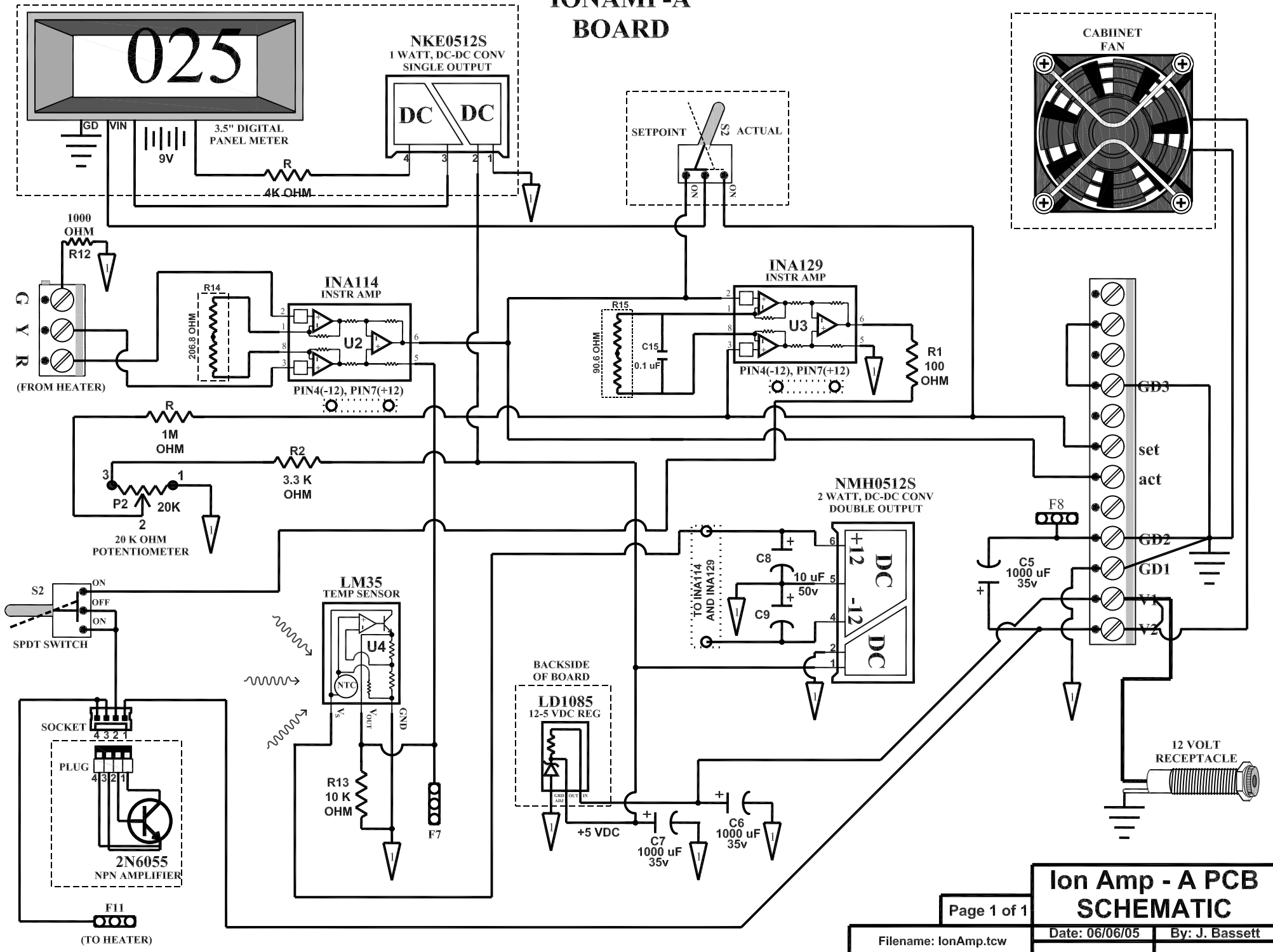
THE LM324's IN THIS CIRCUIT OPERATE USING +12V ON PIN 4 AND -12V ON PIN 11.

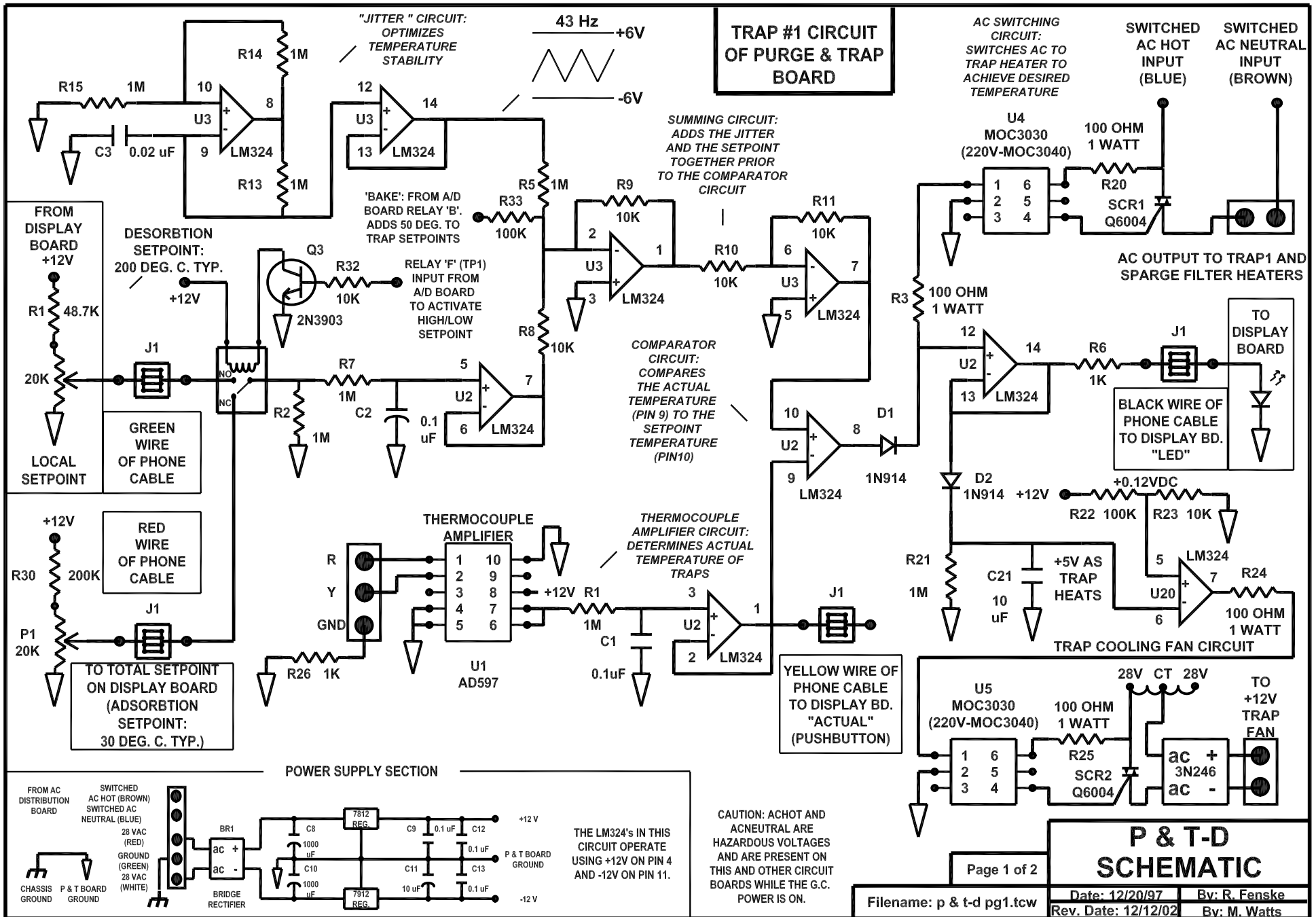
DUAL OVEN-G #2 SCHEMATIC

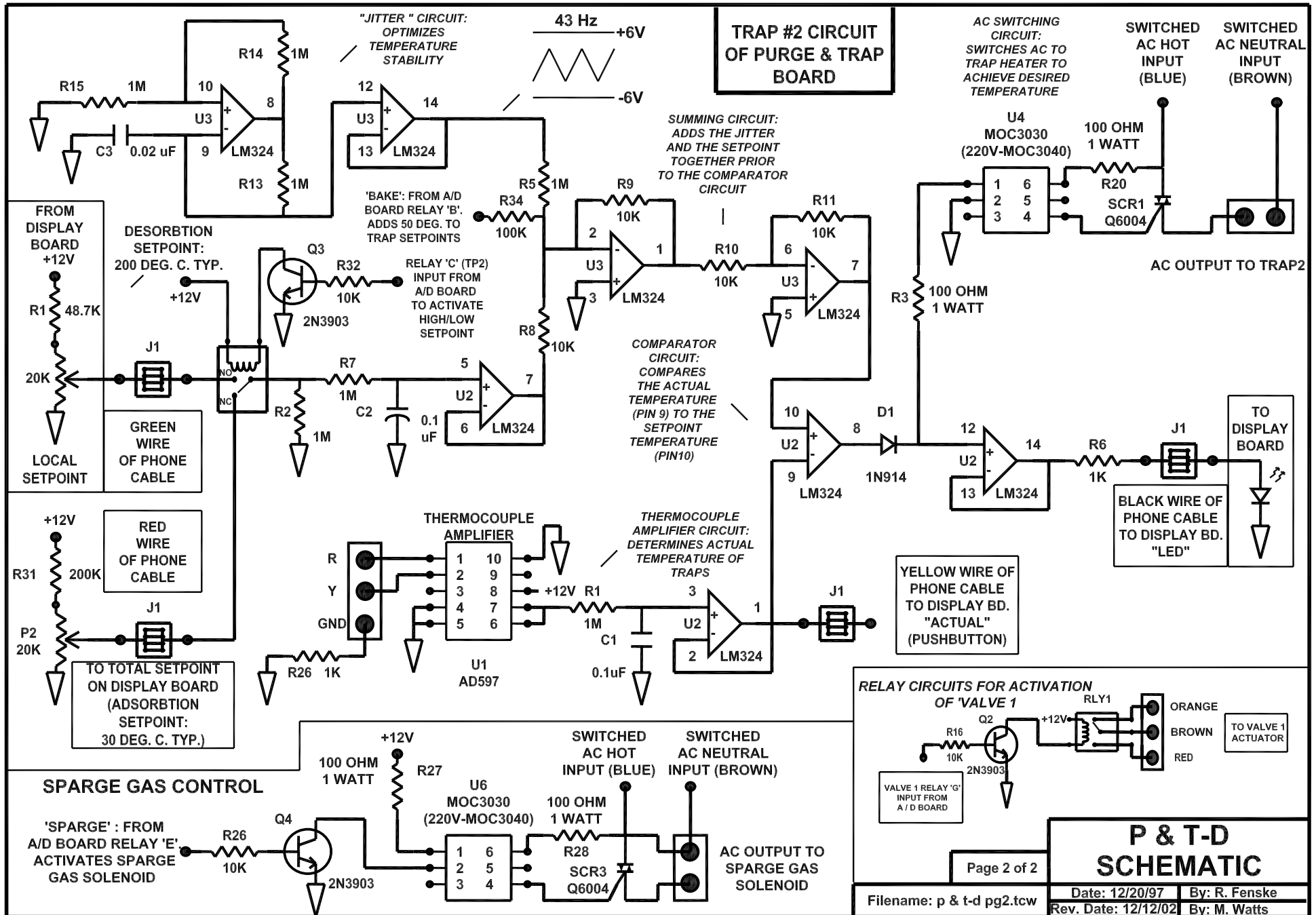
Page 3 of 3

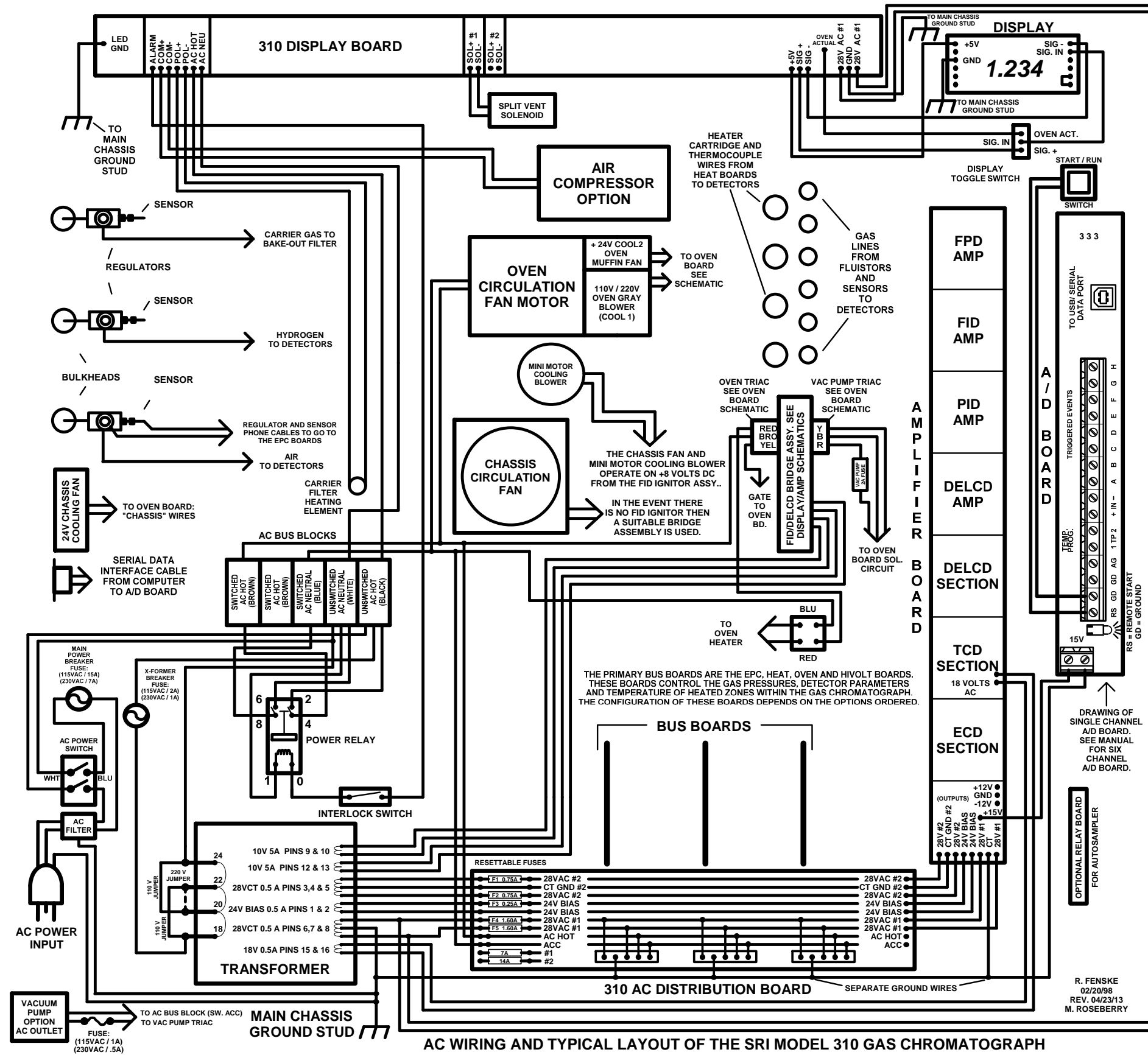
Filename: dual oven-g #2 pg3.tcw	Date: 7/16/03	By: M.Watts
	Rev. Date: 12/16/03	By: M.Watts

IONAMP-A BOARD







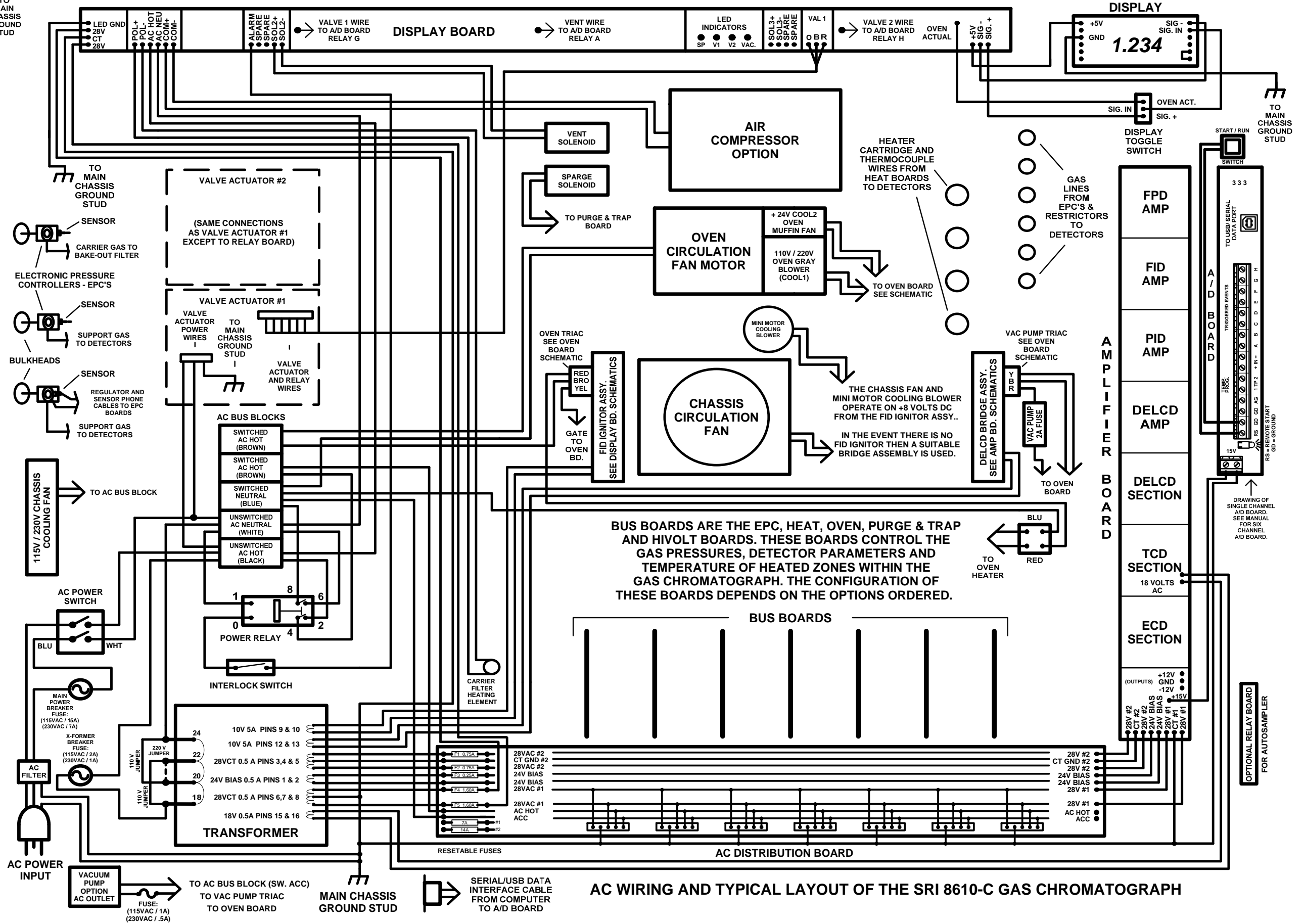


AC WIRING AND TYPICAL LAYOUT OF THE SRI MODEL 310 GAS CHROMATOGRAPH



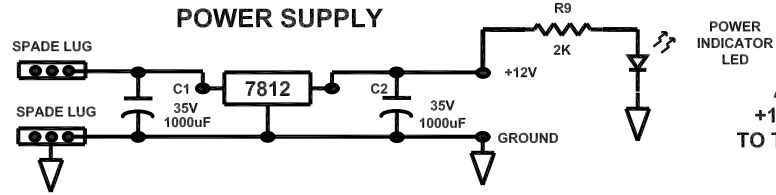
TO MAIN CHASSIS GROUND STUD

TO MAIN CHASSIS GROUND STUD

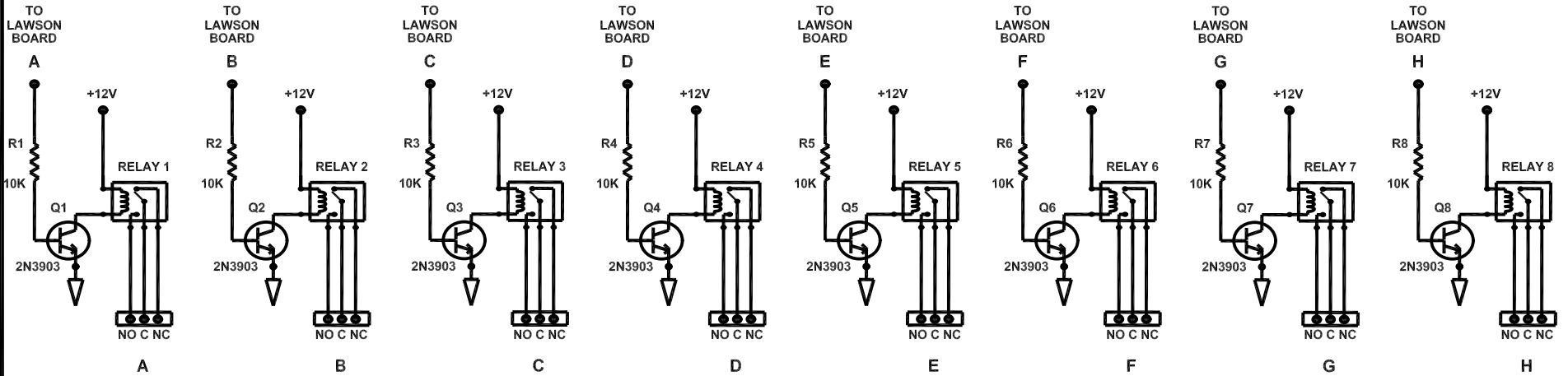


AC WIRING AND TYPICAL LAYOUT OF THE SRI 8610-C GAS CHROMATOGRAPH

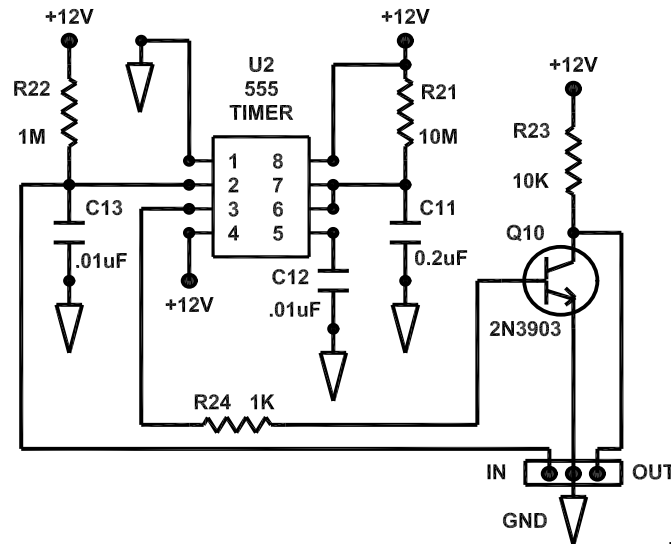
15 - 24 Vdc INPUT FROM
WALL TRANSFORMER



ALSO SUPPLIES
+12 V AND GROUND
TO THE 203 A/D BOARD

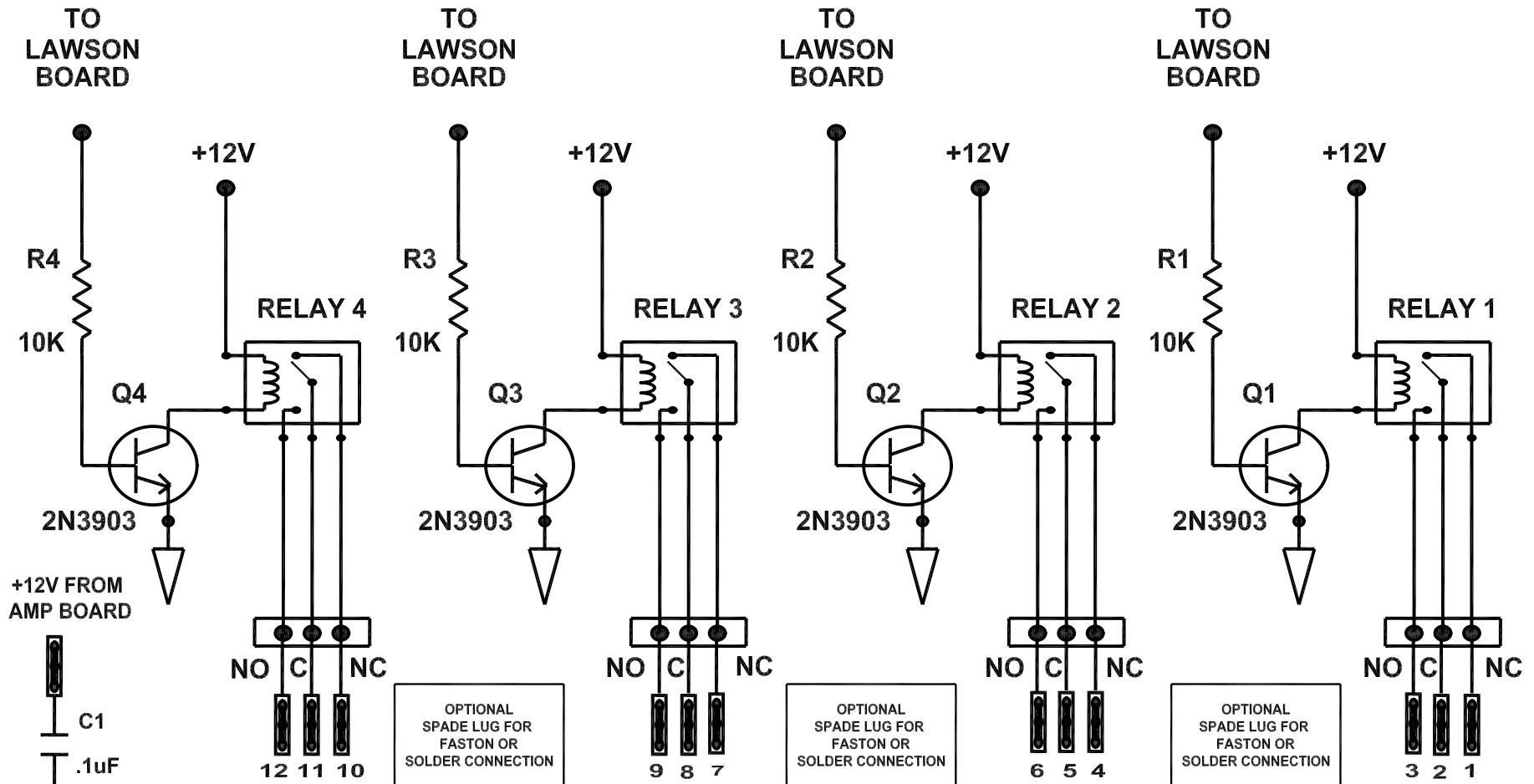


REMOTE START CIRCUIT
(MOMENTARY GROUNDING OF THE INPUT
PRODUCES A 2 SECOND NEGATIVE GOING PULSE
ON THE OUTPUT WHICH TRIGGERS THE
203 A/D BOARD REMOTE START FUNCTION.)



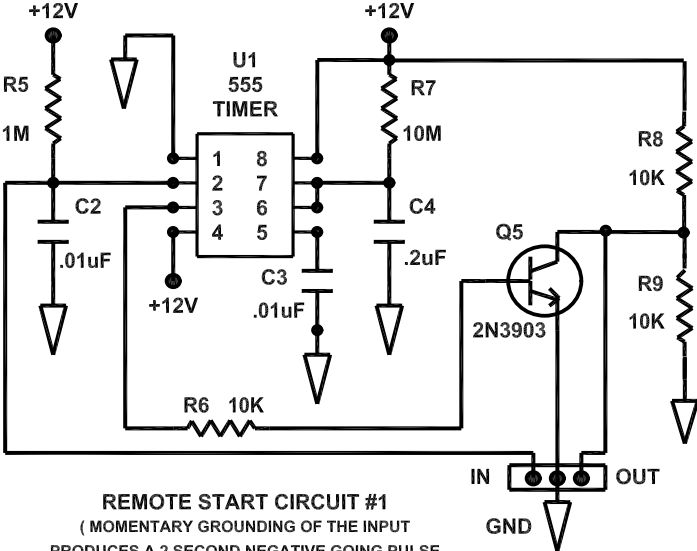
OPTIONAL RELAY CIRCUITS:
USED WHEN A CONTACT CLOSURE
IS NEEDED FOR ACTIVATION
OF EXTERNAL DEVICES.

ACCESSORY RELAY BOARD (1-4 RELAYS) WITH EXTERNAL CONNECTOR

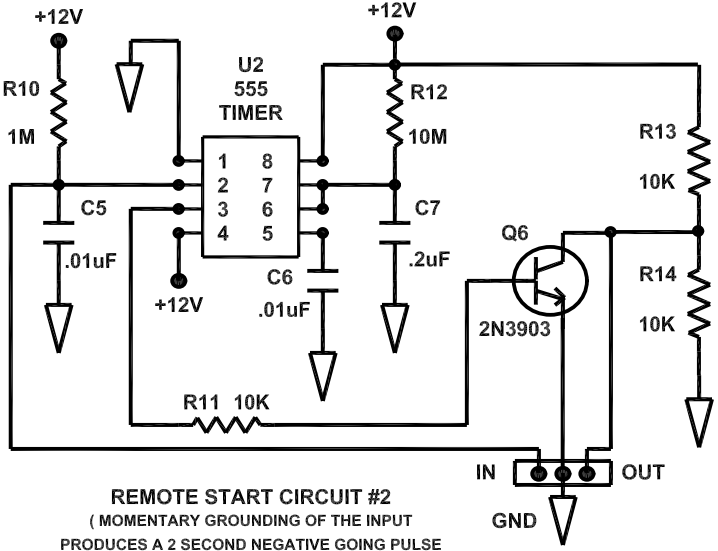


CHASIS GROUND

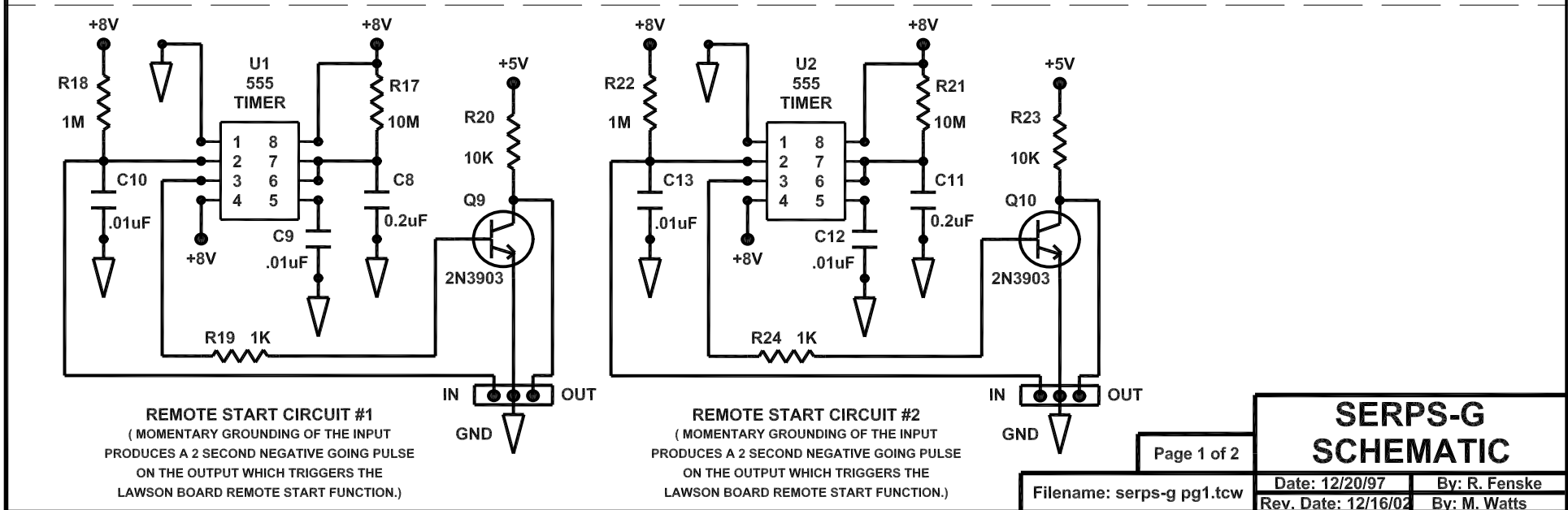
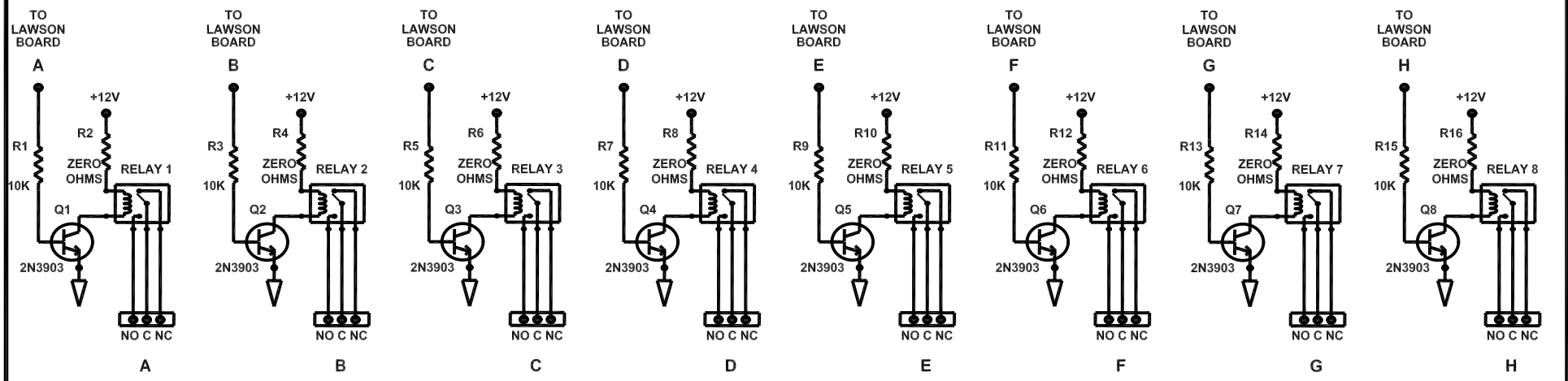
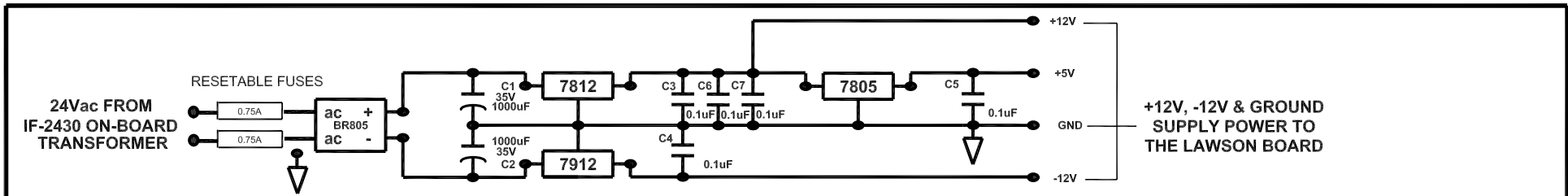
DUAL REMOTE START PULSE STRETCHER



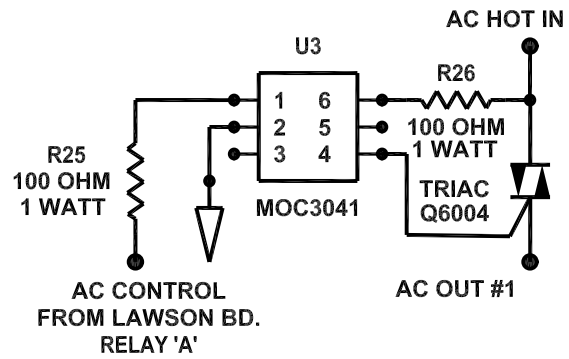
REMOTE START CIRCUIT #1
 (MOMENTARY GROUNDING OF THE INPUT
 PRODUCES A 2 SECOND NEGATIVE GOING PULSE
 ON THE OUTPUT WHICH TRIGGERS THE
 LAWSON BOARD REMOTE START FUNCTION.)



REMOTE START CIRCUIT #2
 (MOMENTARY GROUNDING OF THE INPUT
 PRODUCES A 2 SECOND NEGATIVE GOING PULSE
 ON THE OUTPUT WHICH TRIGGERS THE
 LAWSON BOARD REMOTE START FUNCTION.)



OPTIONAL AC OUTPUT CIRCUIT #1



OPTIONAL AC OUTPUT CIRCUIT #2

